

« Kennedy_Zhang »

Pin	Pin Name	Signal Name	Type
32	PWM0/GPA0	/	
33	PWM1/GPA1	FAN_PWM	O
36	PWM2/GPA2	/	
37	PWM3/GPA3	/	
38	PWM4/GPA4	CHG_LED_UP#	O
39	PWM5/GPA5	PWR_LED_UP#	O
40	PWM6/GPA6	/	
43	PWM7/GPA7	LCD_BACKOFF#	O
153	RXD/GPB0	NUM_LED	O
154	TXD/GPB1	CAP_LED	O
162	GPB2	SCRL_LED	O
163	SMCLK0/GPB3	SMB0_CLK	I/O
164	SMDAT0/GPB4	SMB0_DAT	I/O
5	GA20/GPB5	A20GATE	O
6	KBRST#/GPB6	RC_IN#	O
165	GPB7	THRO_CPU	O
165	CLKOUT/GPC0	/	
169	SMCLK1/GPC1	SMB1_CLK	I/O
170	SMDAT1/GPC2	SMB1_DAT	I/O
171	GPC3	/	
172	TMRI0/WUI2/GPC4	ACIN_OC#	I
175	GPC5	OP_SD#	O
176	TMRI1/WUI3/GPC6	BAT_IN_OC#	I
1	CK32KOUT/GPC7	EC_IDE_RST#	O
26	RI#/WUI0/GPD0	SUSB#	I
29	RI2#WUI1/GPD1	SUSC#	I
30	LPCRST#/WUI4/GPD2	BUF_PLT_RST#	O
31	ECSC#/GPD3	EXT_SC#	O
41	GPD4	RF_ON_SW#	I
42	GIINT/GPD5	/	
62	TACH0/GPD6	FAN0_TACH	I
63	TACH1/GPD7	GAIN_AMP#_K	O
87	ADC4/GPE0	COLOREN#	I
88	ADC5/GPE1	INTERNET#	I
89	ADC6/GPE2	MARATHON#	I
90	ADC7/GPE3	DISTP#	I
2	PWR5W/GPE4	PWR_SW#	I
44	WUI5/GPE5	/	
24	LPCPD#/WUI6/GPE6	LID_EC#	I
25	CLKRUN#/WUI7/GPE7	/	
110	PS2CLK0/GPF0	/	
111	PS2DAT0/GPF1	/	
114	PS2CLK1/GPF2	/	I/O
115	PS2DAT1/GPF3	/	I/O
116	PS2CLK2/GPF4	TP_CLK	I
117	PS2DAT2/GPF5	TP_DAT	I
118	PS2CLK3/GPF6	/	
119	PS2DAT3/GPF7	INSTANTON#	I
113	FA16/GPG0	FA16	I
112	FA17/GPG1	FA17	I
104	FA18/GPG2	FA18	I
103	FA19/GPG3	/	
3	FA20/GPG4	THRM_CPU#	I
4	FA21/GPG5	/	
27	LPC80HL/GPG6	PMTHERM#	O
28	LPC80LL/GPG7	AC_APPR_UC#	I

Pin	Pin Name	Signal Name	Type
48	GPH0	VBUS_ON	O
54	GPH1	VBUS_CD#	I
55	GPH2	CPUPWR_GDN	I
69	GPH3	PM_PWRBTN#	O
70	GPH4	SUSC_ON	O
75	GPH5	SUSB_ON	O
76	GPH6	CPU_VRON	O
105	GPH7	PM_RSMRST#	O
148	GPI0	ICH7_PWROK	O
149	GPI1	WATCH_DOG#	O
152	GPI2	/	
155	GPI3	CHG_EN#	O
156	GPI4	PRECHG	O
168	GPI5	BAT_LL#	O
174	GPI6	BAT_LEARN	O

ICH8-M GPIO SETTING

Pin	Pin Name	Signal Name	Type
AB18	GPI000/BM_BUSY#	PM_BMBUSY#	I
C8	GPI001/REQ5#	PCI_REQ#5	I
G8	GPI002/PIRQ#	PCI_INTE#	I
F7	GPI003/PIRQ#	PCI_INTF#	I
F8	GPI004/PIRQ#	PCI_INTG#	I
G7	GPI005/PIRQ#	PCI_INTH#	I
AC21	GPI006	BTLED_ON	I/O
AC18	GPI007	WLA_LED_EN	I
E21	GPI008	EXTSM#	I
E20	GPI009	SATA_DET#0	I
A20	GPI010	WLAN_ON#	O
B23	SMBALERT#/GPI011	SMB_ALERT#	I
F19	GPI012	KBC_SCI#	I
E19	GPI013	/	
R4	GPI014	/	
E22	GPI015	CB_SD#	I/O
AC22	GPI016	PM_DPRSLPVR	O
D8	GPI017/GNT5#	PCI_GNT#5	O
AC20	GPI018/STP_PC#	STP_PC#	O
AH18	GPI019/SATA1GP	TP_LEDON	I
AF21	GPI020/STP_CPU#	STP_CPU#	O
AE19	GPI021/SATA0GP	CPU_Select	O
A13	GPI022/REQ4#	PCI_REQ#4	I
AA5	LDRQ1#/GPI023	LPC_DRQ#1	I/O
R3	GPI024	/	
D20	GPI025	/	
A21	GPI026/EL_RSVD	/	
B21	GPI027/EL_STATE0	PD_DET#	I
E23	GPI028/EL_STATE1	/	
C3	GPI029/OC#5	USB_OC#5	I
A2	GPI030/OC#6	NEWCARD_OC#	I
B3	GPI031/OC#7	USB_OC#7	I
AG18	GPI032/CLKRUN#	PM_CLKRUN#	O
AC19	GPI033/AZ_DOCK_EN#	BT_ON#	O
U2	GPI034/AZ_DOCK_RST#	/	
AD21	GPI035	ICH_GPIO35	O
AH19	GPI036/SATA2GP	/	
AE19	GPI037/SATA3GP	PCB_ID0	I/O
AD20	GPI038	PCB_ID1	I
AE20	GPI039	PCB_ID2	I
A14	GNT4#/GPI048	PCI_GNT#4	O
AG24	GPI049/CPUPWRGD	H_PWRGD	O

PCI Device	IDSEL#	REQ/GNT#	Interrupts
CARD READER	AD17	0	A
1394	AD17	0	B


PCIe Device	Bus
L1	PE(T/R)(p/n)1
MINI_CARD Kedron	PE(T/R)(p/n)2
NEWCARD	PE(T/R)(p/n)3
MINI_CARD Robson	PE(T/R)(p/n)4
eSATA	PE(T/R)(p/n)5
MINI_CARD TV	PE(T/R)(p/n)6

SM-Bus Device	SM-Bus Address
Clock Generator	1101001x (D2)
SO-DIMM 0	1010000x (A0)
SO-DIMM 1	1010001x (A2)
CPU Thermal Sensor(MAX6657)	0100110x (4C)
VGA Thermal Sensor(MAX6649)	0100110x (4C)

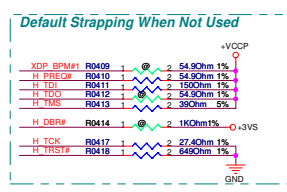
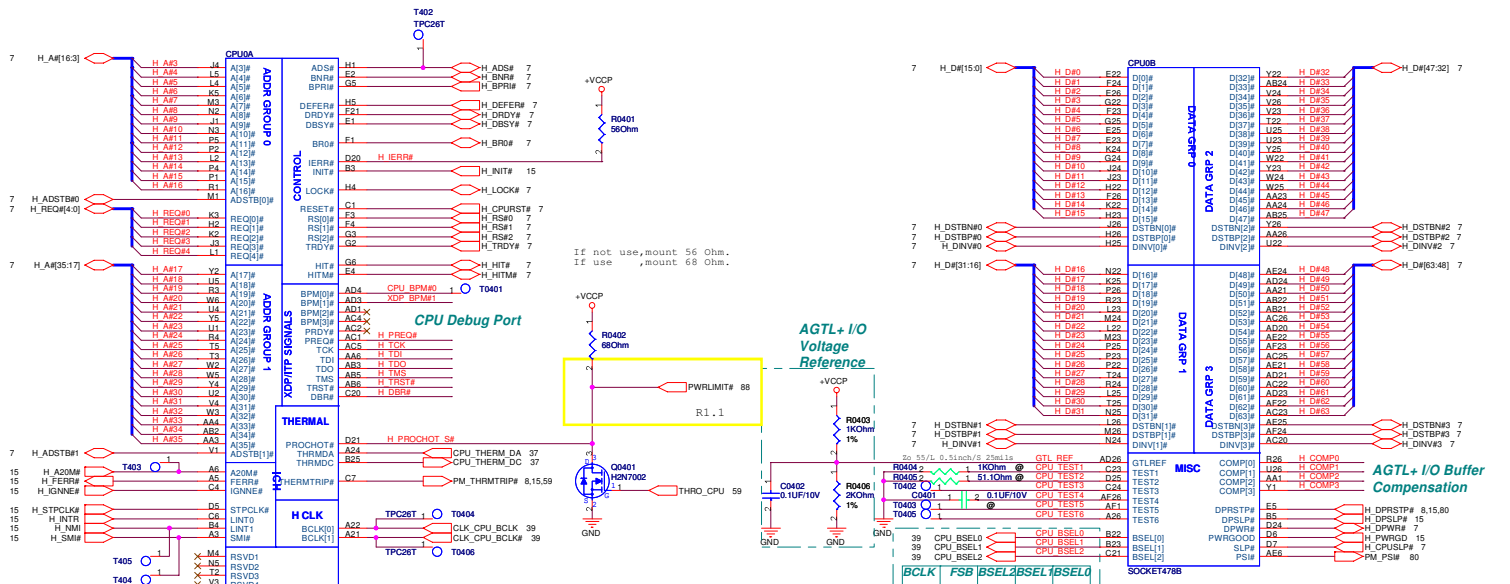
Pins	Pin Name	Devices
3	PCICLK3	TPM
4	PCICLK4	CardBus R5C832
5	SELPCIE_X0_LCD#PCICLK5	EC I78510E
8	ITP_EM#PCICLK_P0	ICH7
9	SELLCD_27#PCICLK_F1	x
12	FSLA/USB_48MHz	ICH7
14	DOTT_96MHz	x
15	DOTC_96MHz	x
17	27FX/LCD_SSCGT/PCIE_X0T	G72/G73
18	27SS/LCD_SSCGC/PCIE_X0C	G72/G73
19	PCIE_XT1	x
20	PCIE_XT1	x
22	PCIE_XT2	ICH7
23	PCIE_XT2	ICH7
24	PCIE_XT3	MCH
25	PCIE_XT3	MCH
26	SATA_CLKT	ICH7
27	SATA_CLKC	ICH7
30	PCIE_XT4	LAN
31	PCIE_XT4	LAN
35	PCIE_XT5	G72/G73
36	PCIE_XT5	G72/G73
38	PCIE_XT6	MINICARD
39	PCIE_XT6	MINICARD
40	PERBQ#PCIE_XT7	NEWCARD
41	PERBQ1#PCIE_XT7	MINICARD
43	CFUCLKC2_ITP/PCIE_XT8	NEWCARD
44	CFUCLKT2_ITP/PCIE_XT8	NEWCARD
48	CFUCLKC1	MCH
49	CFUCLKT1	MCH
51	CFUCLKC0	CPU
52	CFUCLKT0	CPU
57	X2	14.318
58	X1	14.318
60	REFP	ICH7
61	REFP/FSLC/TEST_SEL	x

ASUS		Title : <Title>	
ASUSTek Computer INC. NBI		Engineer: Shumin, Frank	
Site	Project Name	Rev	
Custom	A7S	1.0	
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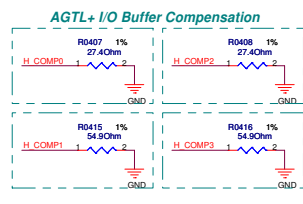
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		Title : Schematic page name	
ASUSTEK COMPUTER INC		Engineer: Shumin, Frank	
Site	Project Name		Rev
Custom	ATS		1.0
Date: 8/18/2007		Sheet: 3	of 88

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BCLK	FSB	BSEL2	BSEL1	BSEL0
166	667	L	H	H
200	800	L	H	L



ASUS		Title : CPU-Merom(HOST)	
ASUSTeK COMPUTER INC. N61		Engineer: Shumin, Frank	
Size: Custom	Project Name: AT5	Date: 8/19/2007	Sheet: 4 of 88
Rev: 1.0			

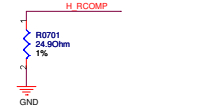
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ASUSTEK COMPUTER INC		Engineer: Shumin, Frank	
Site	Project Name		Rev
Custom	ATS		1.0
Date: 8/18/2007		Sheet: 6	of 88

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RCOMP

For Calibrating the FSB I/O Buffer



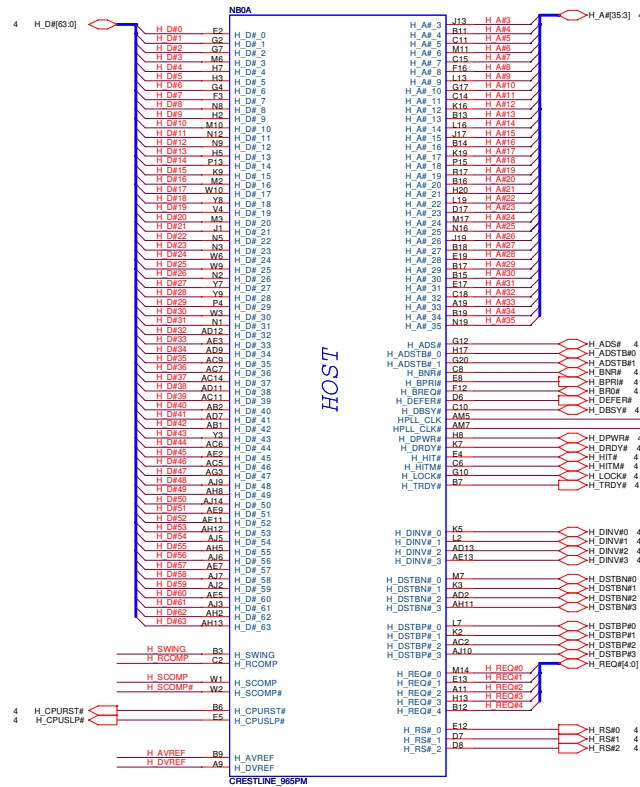
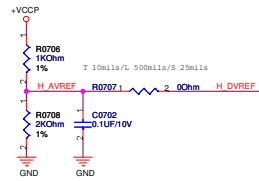
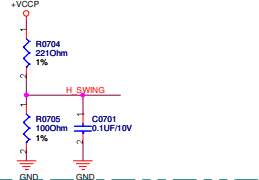
SCOMP

For Slew Rate Compensation on the FSB



Voltage Swing

For Providing a Reference Voltage to The FSB RCOMP circuits



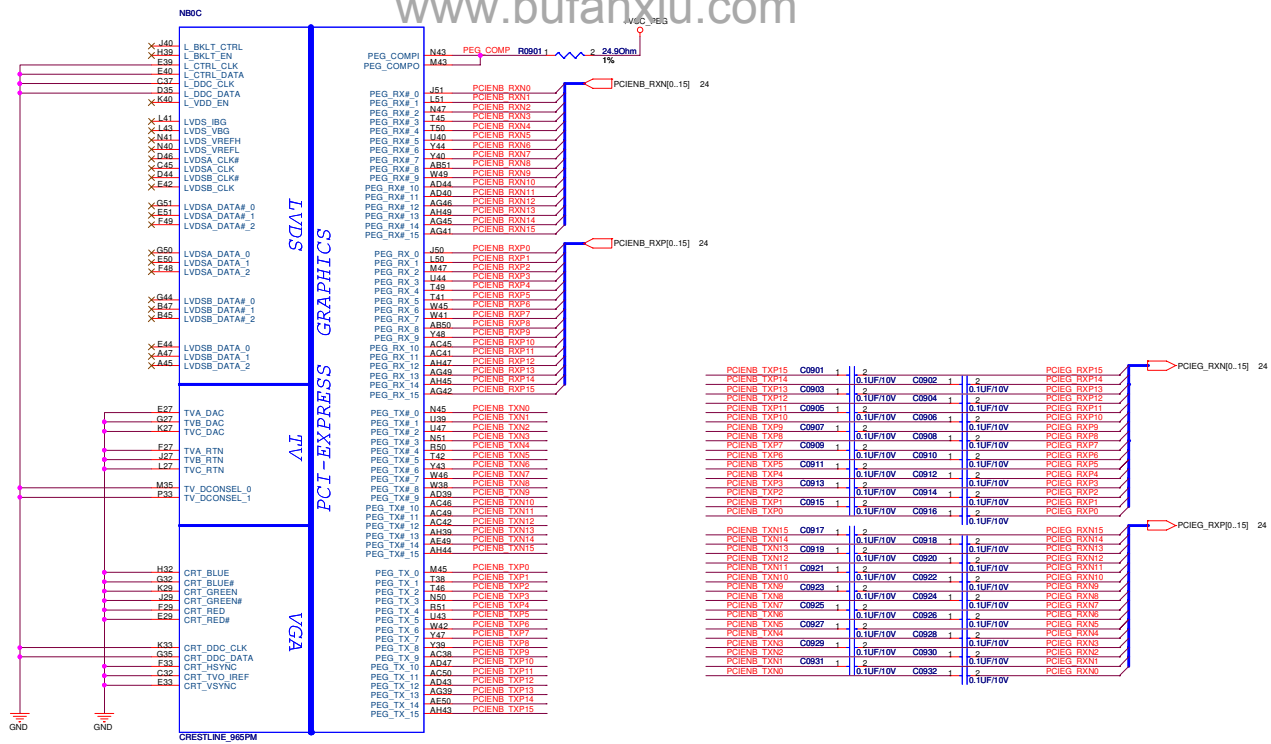
ASUS Title : NB_965PM(HOST)

ASUSTek COMPUTER INC. NBI Engineer: Shumin, Frank

Site: Custom Project Name: A7S Rev: 1.0

Date: 8/11/2007 Sheet: 7 of 9

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R1.1 Remove all 965GM components

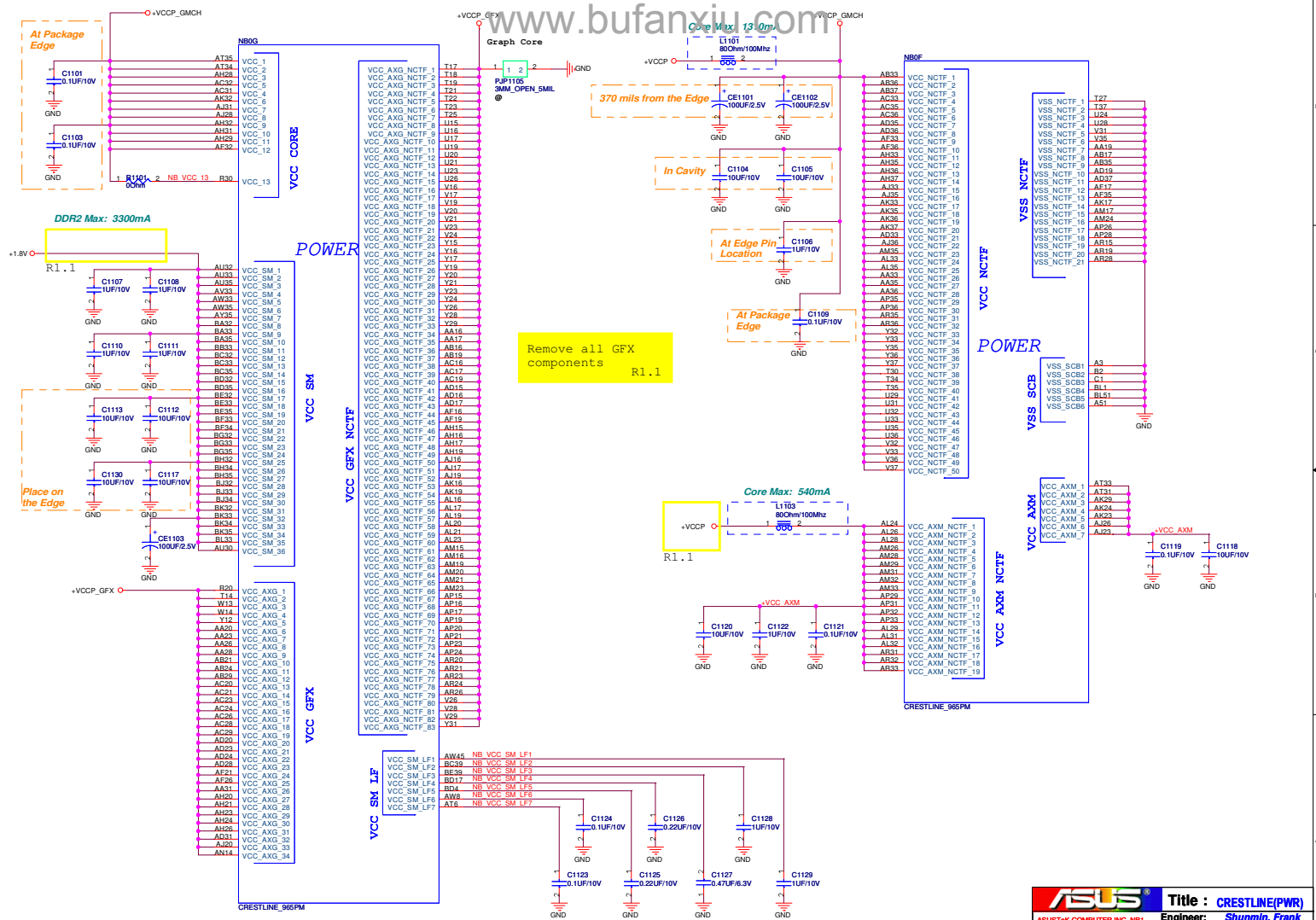
ASUS Title : NB_965PM(GRAPHIC)
 ASUSTeK COMPUTER INC. NBI Engineer: Shumin, Frank
 Site: Project Name: Custom: A7S
 Date: 8/19/2007 Sheet 8 of 88

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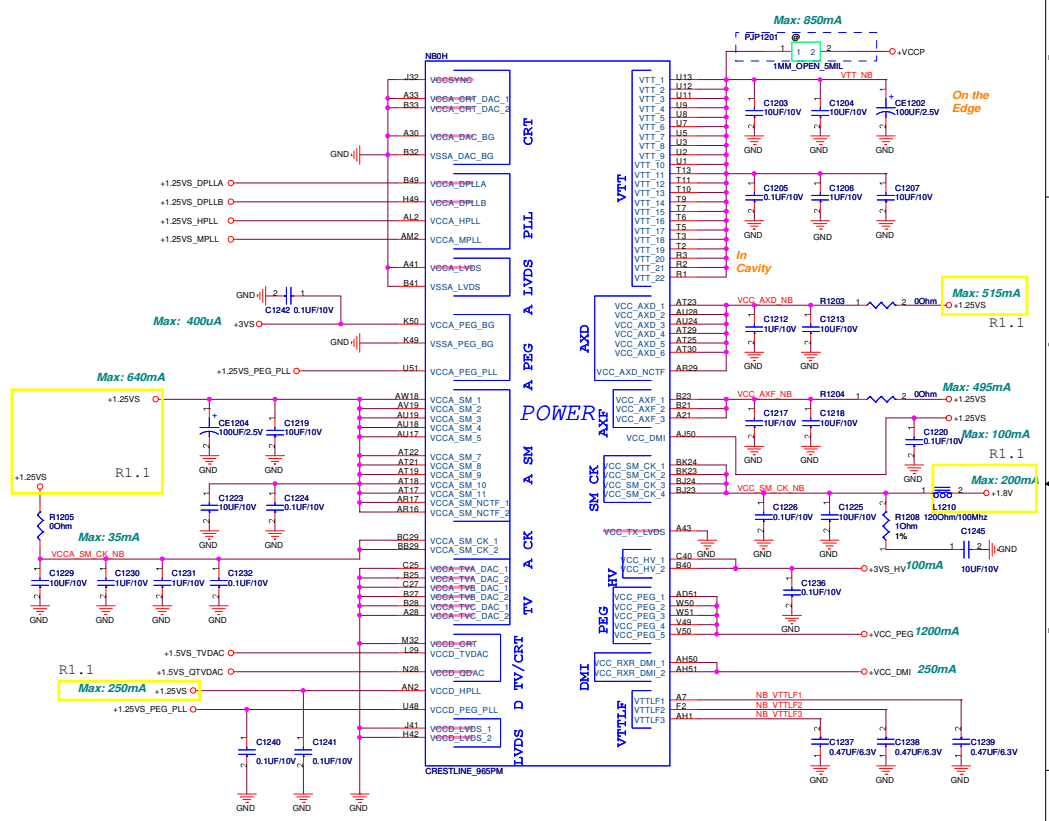
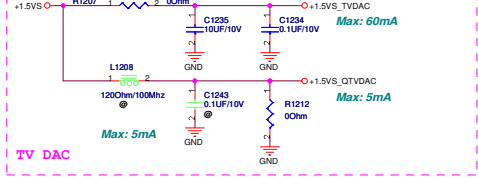
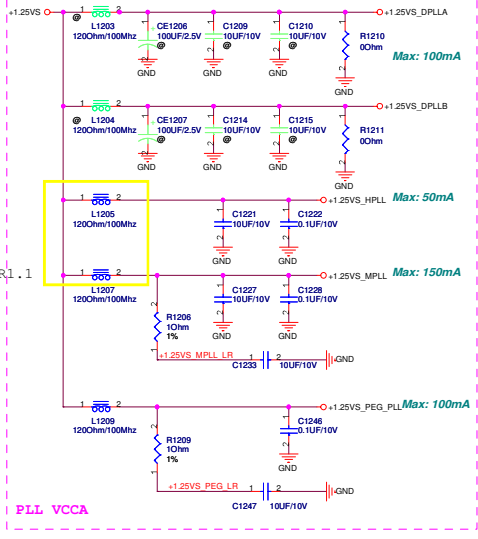
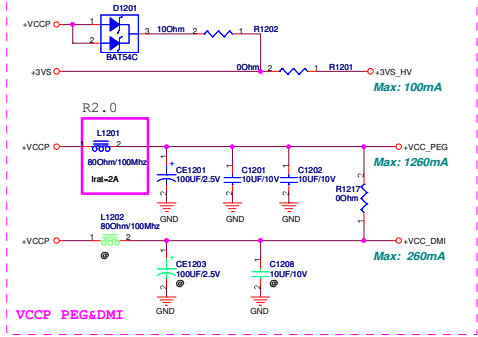
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ASUS		Title : NB_965PM(DDR2)	
ASUSTEK COMPUTER INC. NBI		Engineer: Shumin, Frank	
Site	Project Name	Date	Rev
Custom	ATS	18/11/2007	1.0
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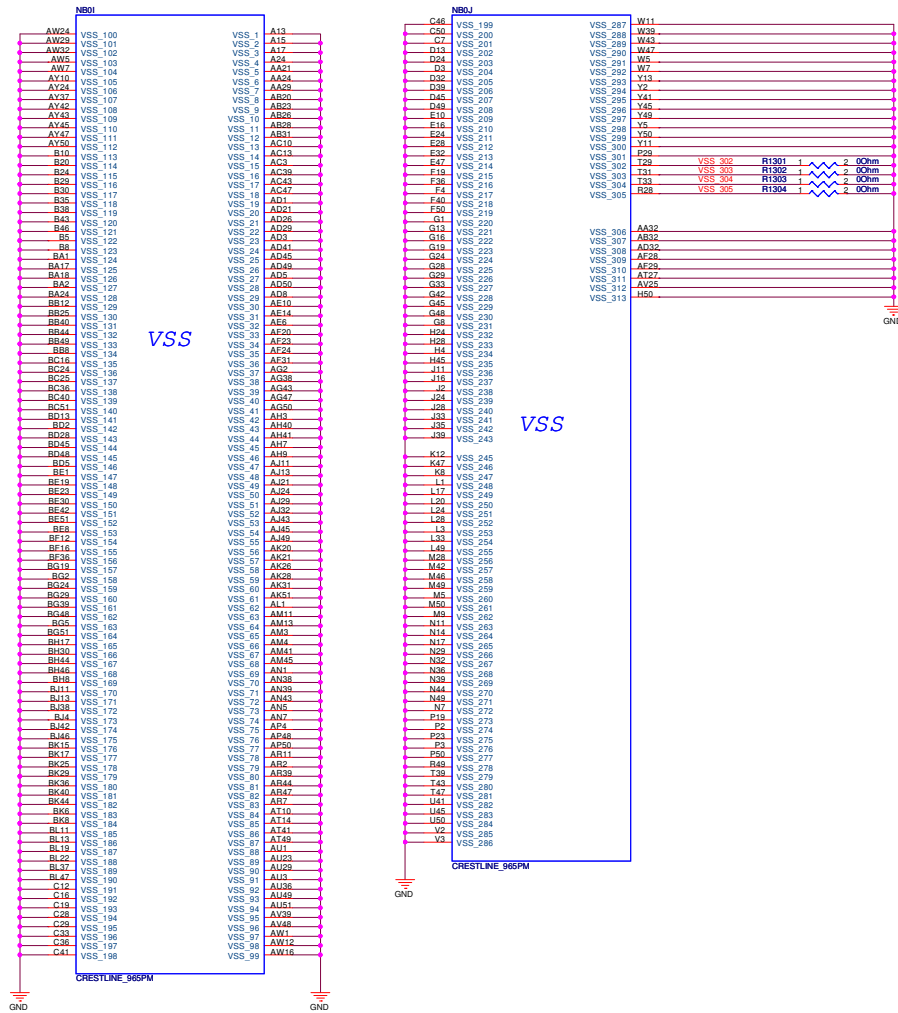
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ASUS		Title : CRESTLINE(PWR)	
ASUSTEK COMPUTER INC. NBI		Engineer: Shumin, Frank	
Site	Project Name	Rev	
Customer	ATS	1.0	
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ASUS		Title : NB_966PM(PWR2)
ASUSTek COMPUTER INC. (NBI)		Engineer: Shunmin, Frank
Size	Project Name	Rev
Custom	ATS	1.0
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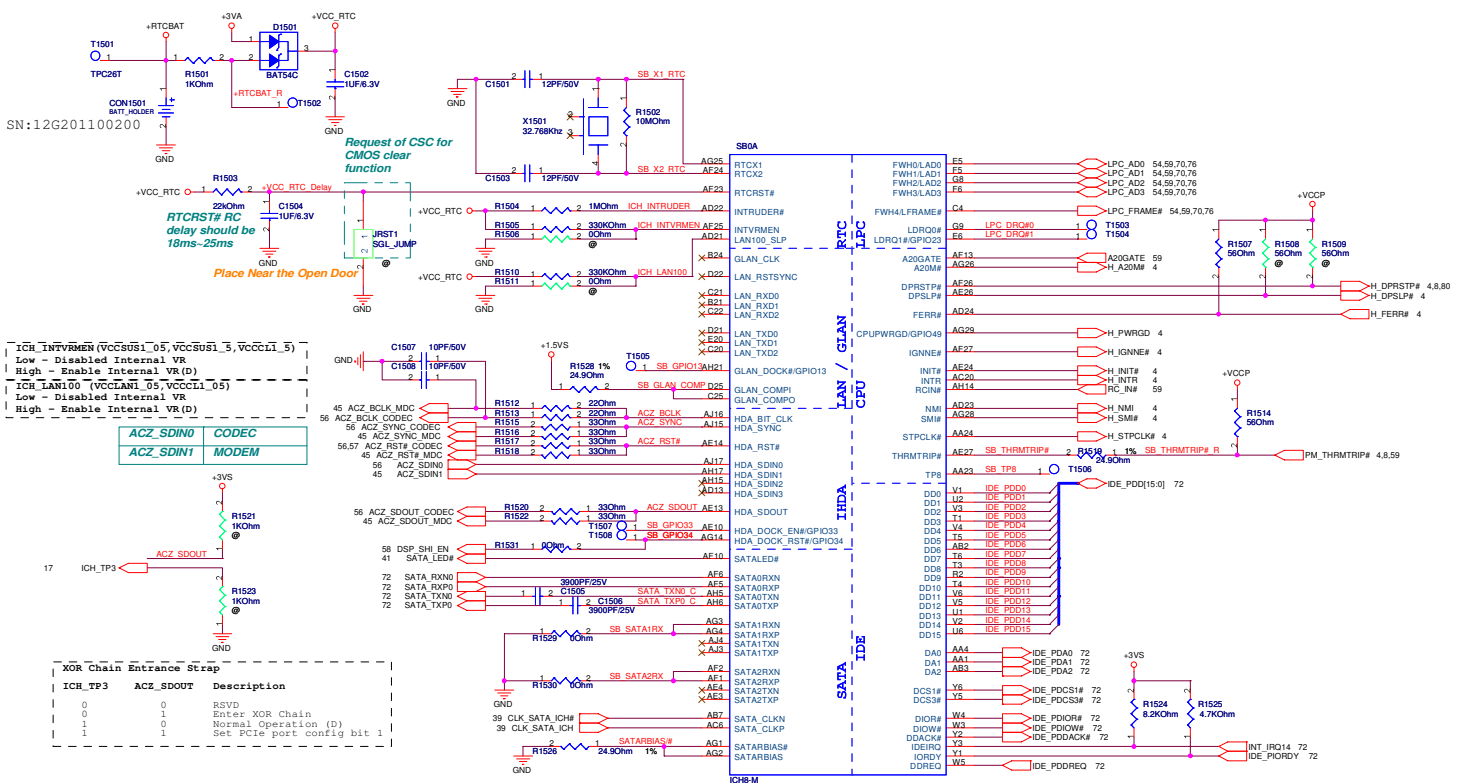


ASUS		Title : CRESTLINE(GND)	
ASUSTEK COMPUTER INC. NBJ		Engineer: Shumin, Frank	
Size	Project Name		Rev
Custom	ATS		1.0
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		Title : Schematic page name	
ASUSTEK COMPUTER INC.		Engineer: Shunmin, Frank	
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SN:12G201100200

ICH_INTVRMEN (VCCSUS1_05, VCCSUS1_5, VCCCL1_5)
 Low - Disabled Internal VR
 High - Enable Internal VR (D)
 ICH_LAN100 (VCCLAN1_05, VCCCL1_05)
 Low - Disabled Internal VR
 High - Enable Internal VR (D)

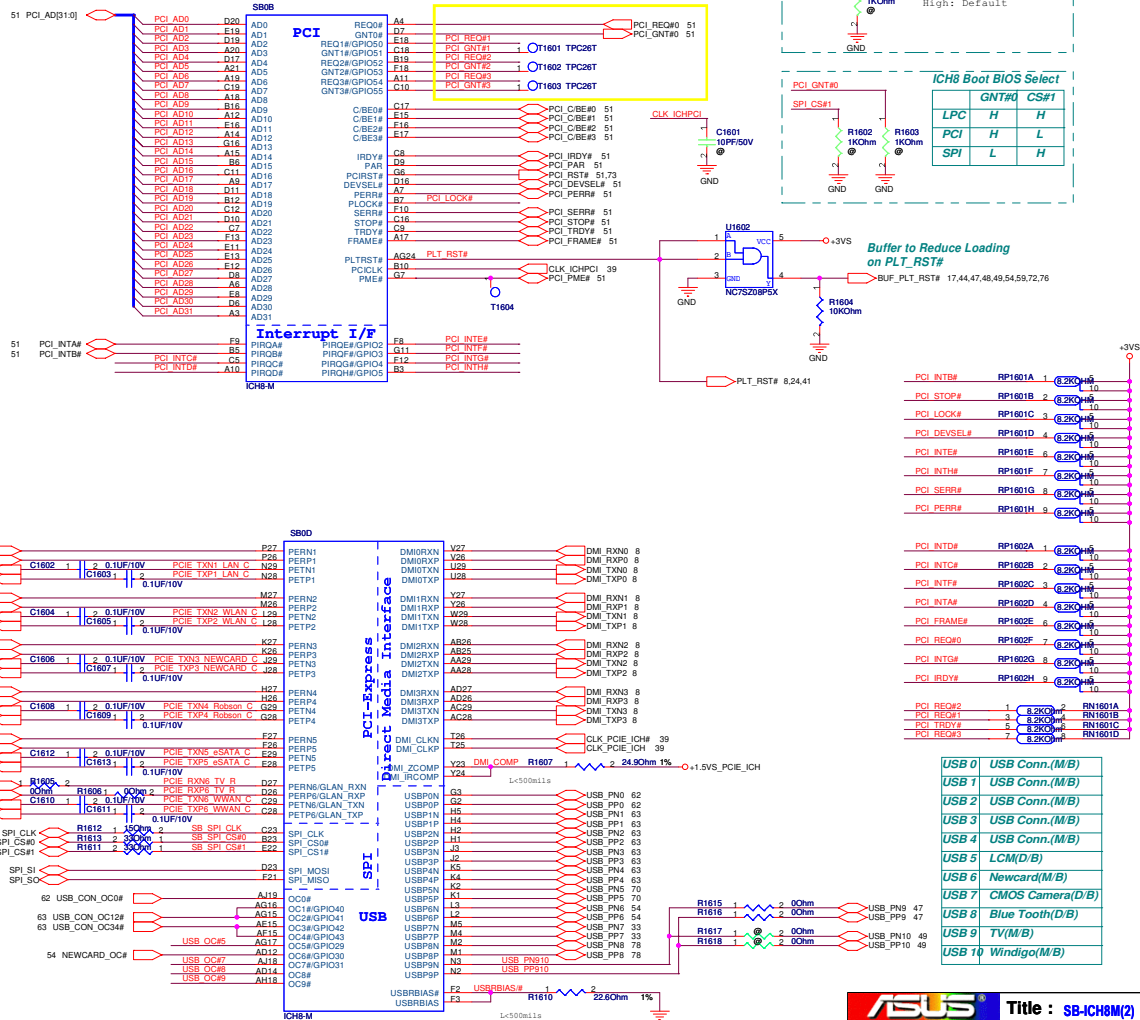
ACZ_SDNIN0	CODECD
ACZ_SDNIN1	MODEMD

XOR Chain Entrance Strap

ICH_TP3	ACZ_SDOUT	Description
0	0	RSVD
0	1	Enter XOR Chain
1	0	Normal Operation (D)
1	1	Set PCIe port config bit 1


ASUS		Title : SB-ICH8M(1)	
ASUSTEK COMPUTER INC. NBI		Engineer: Shumin, Frank	
Site	Project Name	Rev	
Custom	ATS	1.0	
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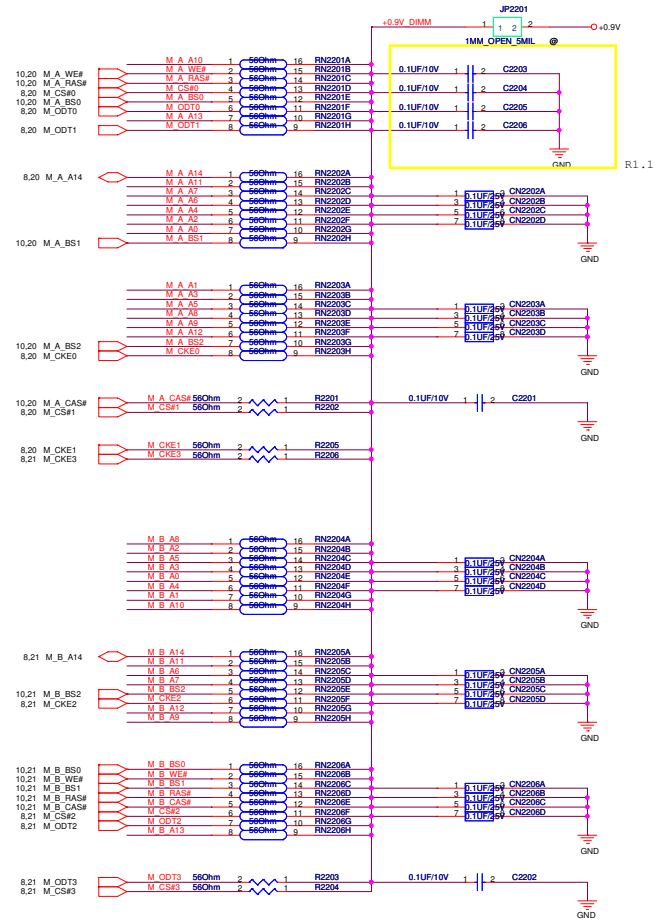
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ASUS Title : SB-ICH8(M)
ASUSTEK COMPUTER INC. NBI Engineer: Shumin, Frank
Size Project Name
Custom A7S
Date: 08-19-2007 Sheet: 16 of 35

		Title : Schematic page name	
ASUSTEK COMPUTER INC		Engineer: Shumin, Frank	
Site	Project Name	Rev	
Custom	ATS	1.0	
Date: 8/18/2007	Sheet 19 of 88		

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10.20 M_A_A[13:7]
10.21 M_B_A[13:7]



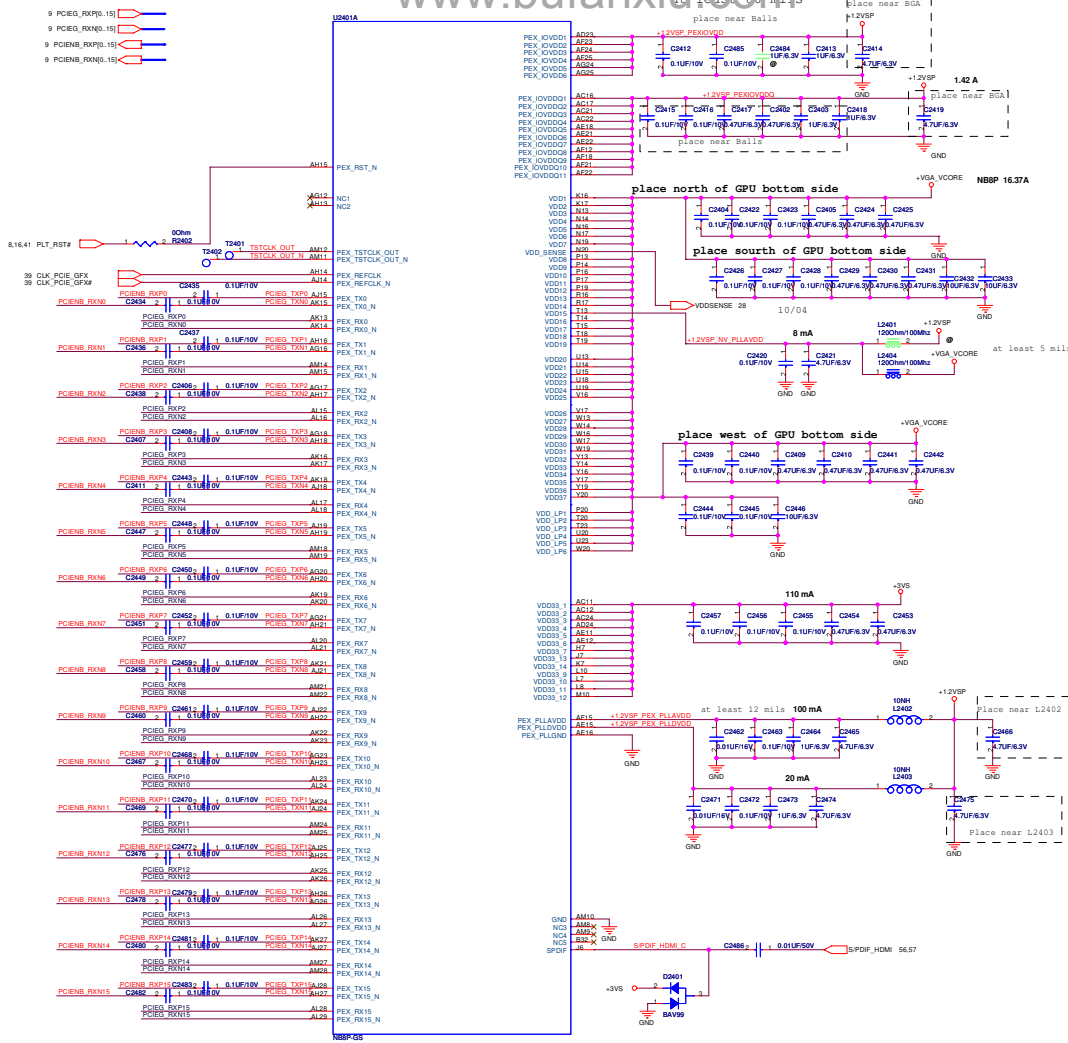
ASUS Title : **DDR2 TERMINATION**
 ASUSTeK COMPUTER INC. (NBI) Engineer: **Shumin, Frank**

Size	Project Name	Rev
Custom	ATS	1.0
Date: 8/18/2007	Sheet: 22	of 35

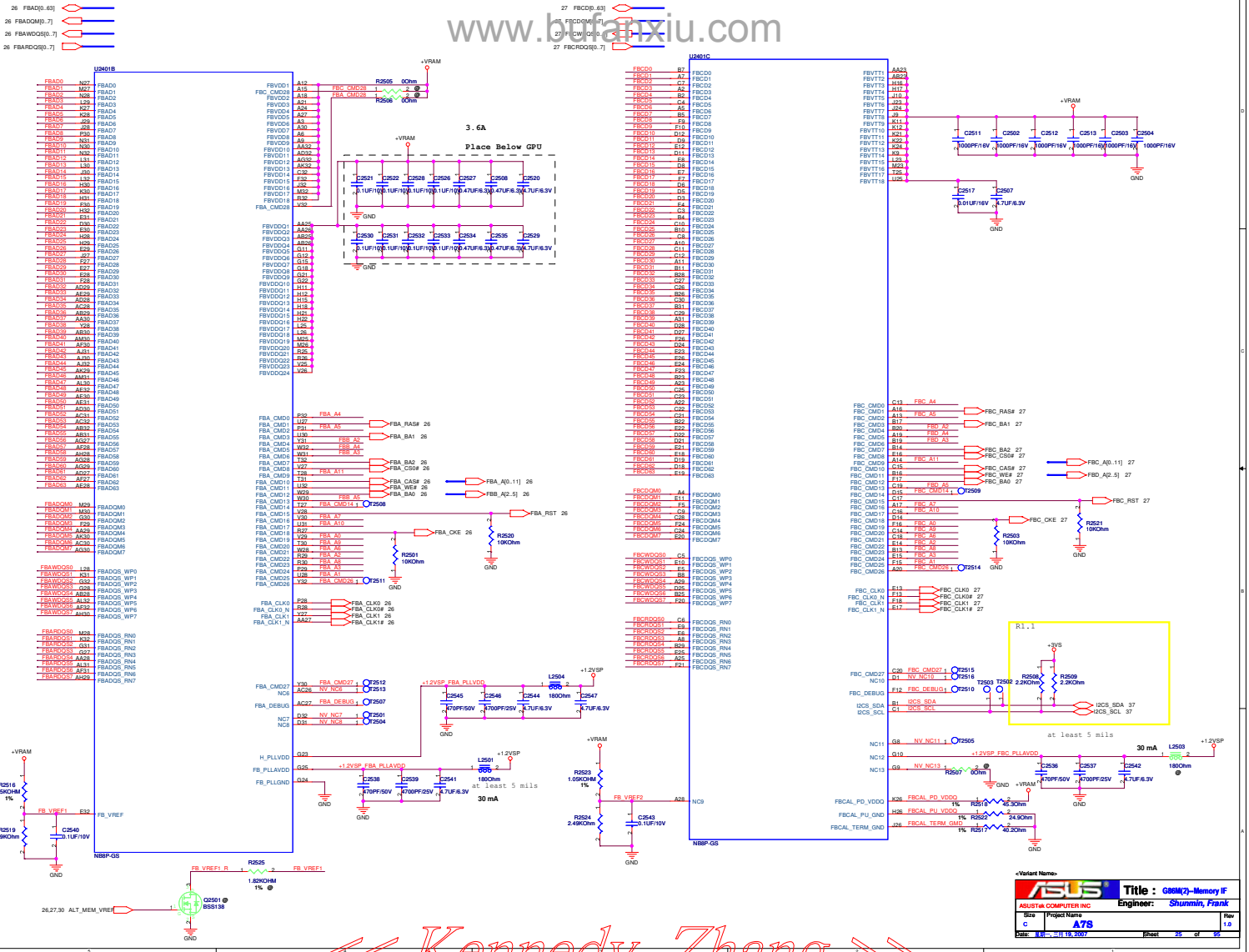
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ASUSTEK COMPUTER INC.		Engineer: Shunmin, Frank	
Size	Project Name		Rev
C	A7S		1.0
Date:	2017-10-19 20:07	Sheet:	23 of 25

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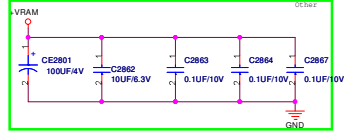
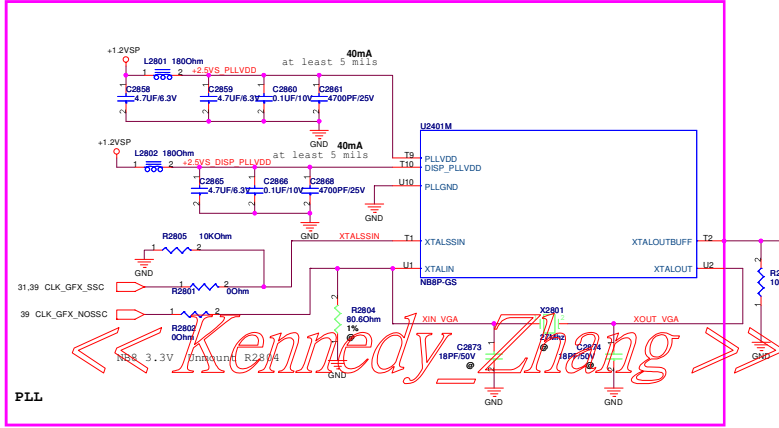
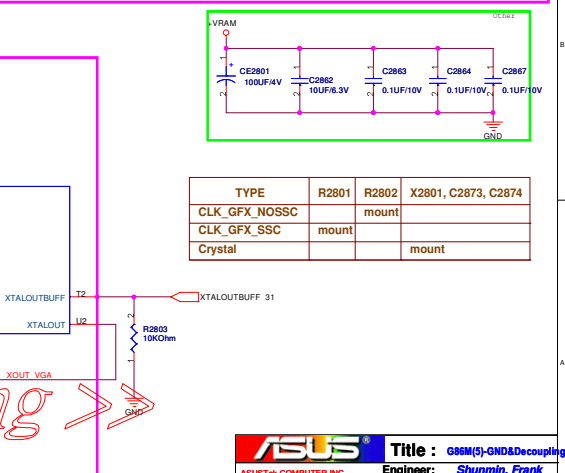
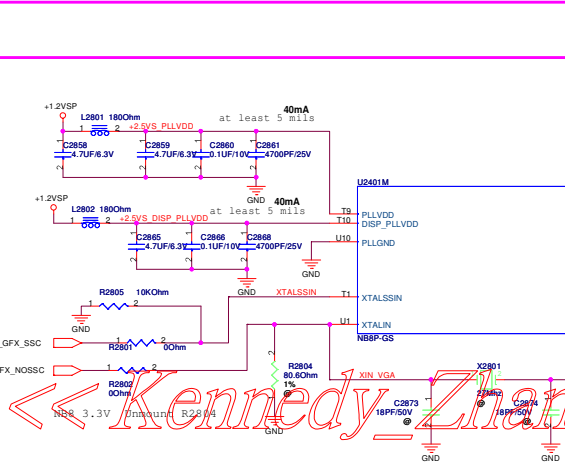
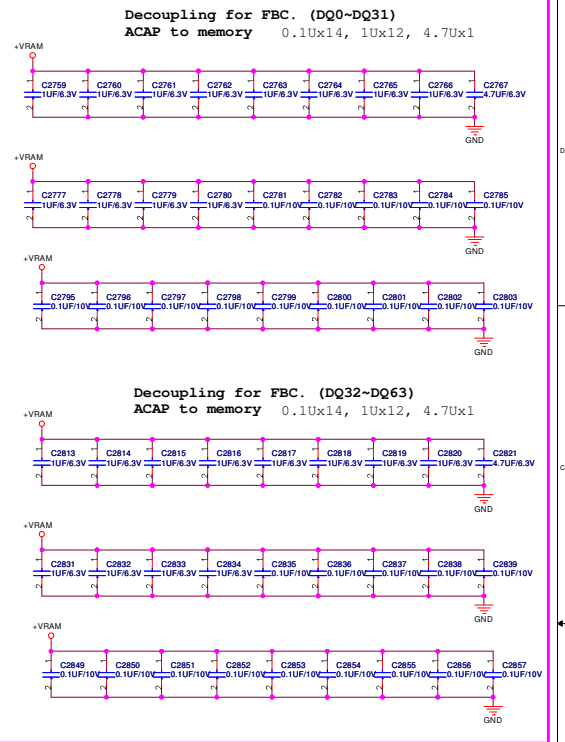
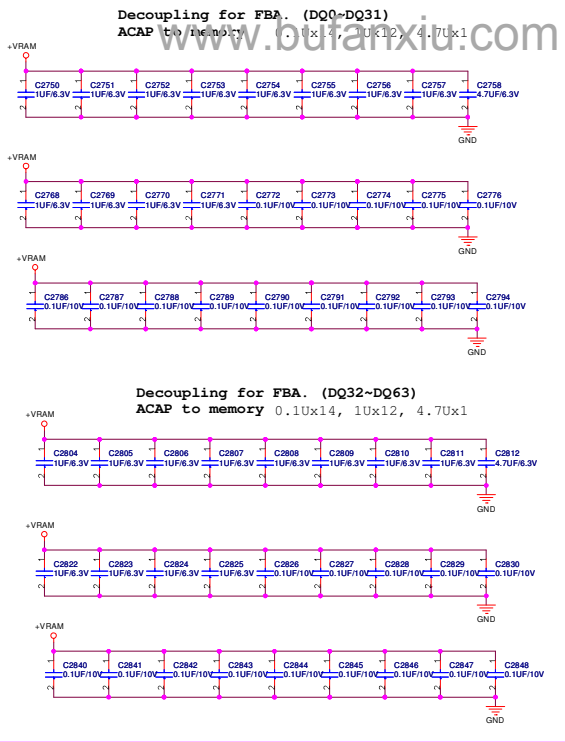
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		Title : G5M7-Memory F	
			Engineer: Shumin, Frank
Srs	Project Name		Rev
C	A78		1.0
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AA12	GND1	GND101	K10
AA13	GND2	GND102	K22
AA14	GND3	GND103	K4
AA15	GND4	GND104	K7
AA16	GND5	GND105	K27
AA17	GND6	GND106	UB
AA18	GND7	GND107	M2
AA19	GND8	GND108	M21
AA20	GND9	GND109	M51
AA21	GND10	GND110	N15
AA22	GND11	GND111	N18
AA23	GND12	GND112	N4
AA24	GND13	GND113	N29
AA25	GND14	GND114	N15
AA26	GND15	GND115	P18
AA27	GND16	GND116	P27
AA28	GND17	GND117	P6
AA29	GND18	GND118	R13
AA30	GND19	GND119	R14
AA31	GND20	GND120	R15
AA32	GND21	GND121	R16
AA33	GND22	GND122	R19
AA34	GND23	GND123	R2
AA35	GND24	GND124	R20
AA36	GND25	GND125	R31
AA37	GND26	GND126	T16
AA38	GND27	GND127	T17
AA39	GND28	GND128	T2
AA40	GND29	GND129	T29
AA41	GND30	GND130	T4
AA42	GND31	GND131	T15
AA43	GND32	GND132	U17
AA44	GND33	GND133	U24
AA45	GND34	GND134	U29
AA46	GND35	GND135	U8
AA47	GND36	GND136	W12
AA48	GND37	GND137	V14
AA49	GND38	GND138	V15
AA50	GND39	GND139	Y29
AA51	GND40	GND140	Y4
AA52	GND41	GND141	V2
AA53	GND42	GND142	V20
AA54	GND43	GND143	V31
AA55	GND44	GND144	W15
AA56	GND45	GND145	W18
AA57	GND46	GND146	W27
AA58	GND47	GND147	W6
AA59	GND48	GND148	V19
AA60	GND49	GND149	V18
AA61	GND50	GND150	Y29
AA62	GND51	GND151	X4
AA63	GND52		
AA64	GND53		
AA65	GND54		
AA66	GND55		
AA67	GND56		
AA68	GND57		
AA69	GND58		
AA70	GND59		
AA71	GND60		
AA72	GND61		
AA73	GND62		
AA74	GND63		
AA75	GND64		
AA76	GND65		
AA77	GND66		
AA78	GND67		
AA79	GND68		
AA80	GND69		
AA81	GND70		
AA82	GND71		
AA83	GND72		
AA84	GND73		
AA85	GND74		
AA86	GND75		
AA87	GND76		
AA88	GND77		
AA89	GND78		
AA90	GND79		
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AA92	GND81		
AA93	GND82		
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AA102	GND91		
AA103	GND92		
AA104	GND93		
AA105	GND94		
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AA108	GND97		
AA109	GND98		
AA110	GND99		
AA111	GND100		

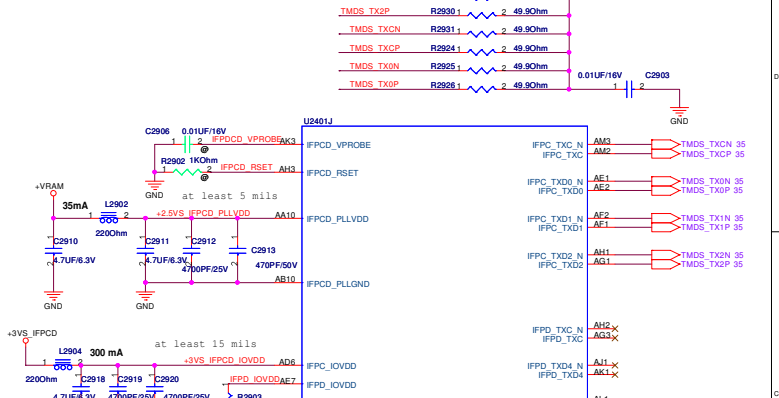
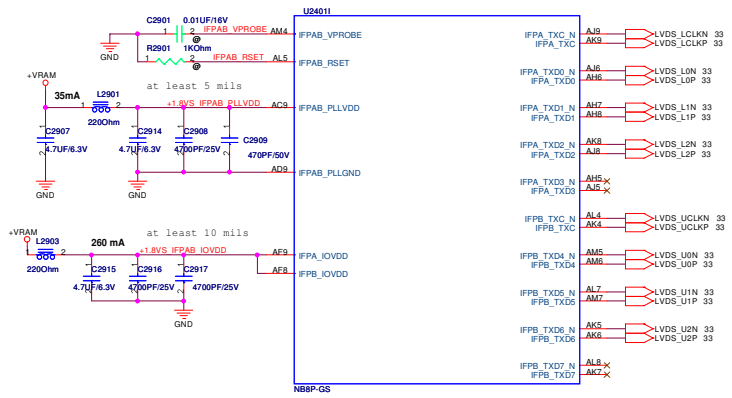


TYPE	R2801	R2802	X2801, C2873, C2874
CLK GFX_NOSSC		mount	
CLK GFX_SSC	mount		
Crystal			mount

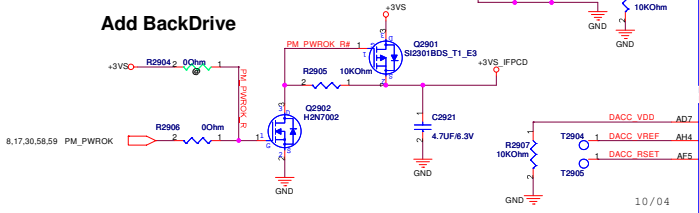
Kennedy Zhang

ASUS Title: G86M(5)-GND&Decoupling
 ASUSTek COMPUTER INC. Engineer: Shunmin, Frank
 Size: Custom Project Name: A7S Rev: 1.0
 Date: 08-11-2007 Sheet: 28 of 65

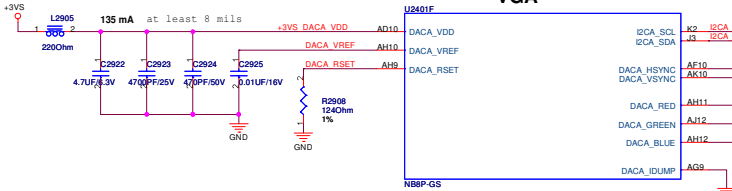
LVDS



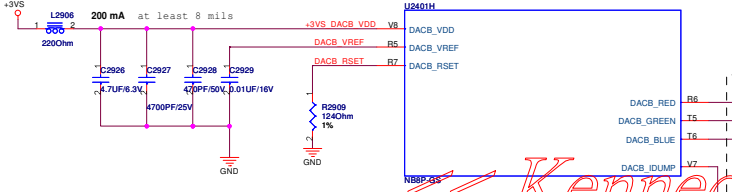
Add BackDrive



VGA

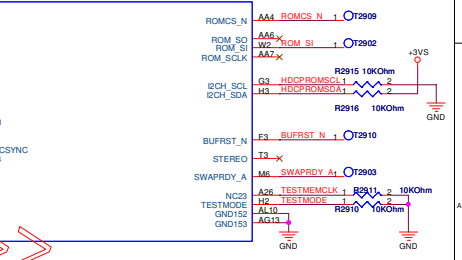


TV



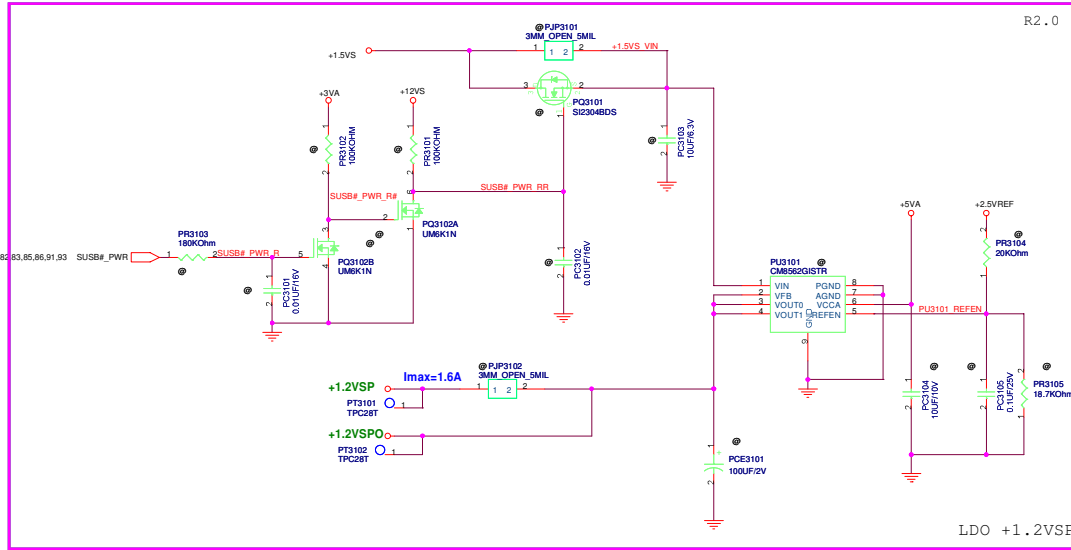
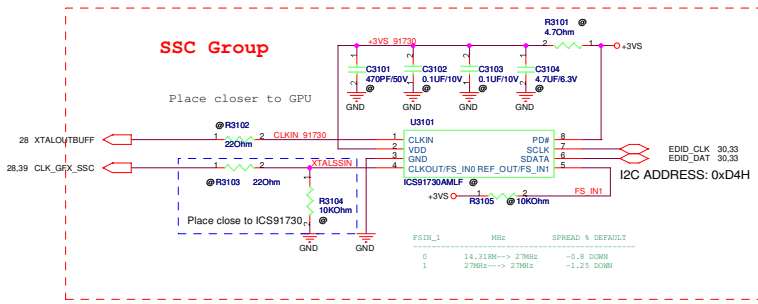
88.7 (HDTV) or 124 (SDTV)

HDCP



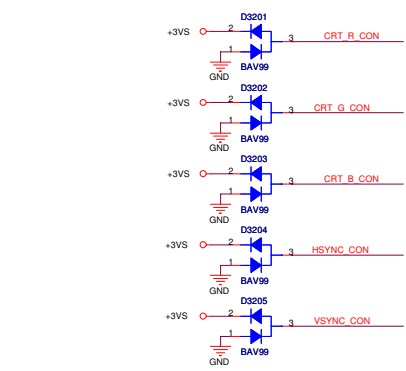
« Kennedy Zhang »

ASUS Title: G86M(6)_LVDS&VGA&TV
 ASUSTek COMPUTER INC Engineer: Shunmin, Frank
 Size Project Name
 Custom A7S Rev 1.0
 Date: 08-19-2007 Sheet: 29 of 65

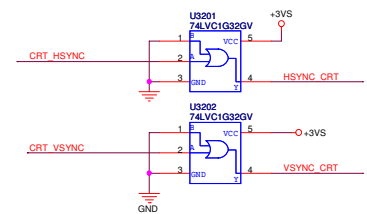
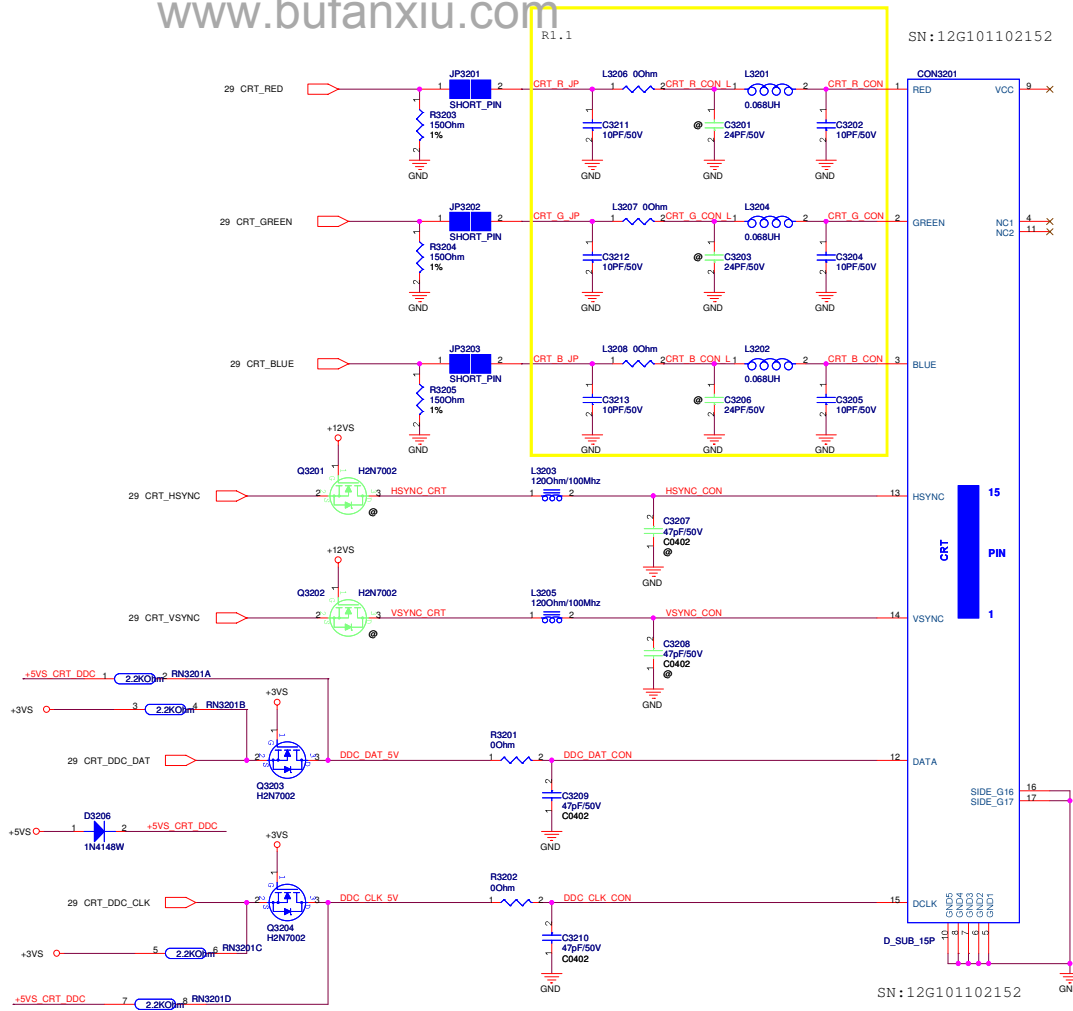


ASUS		Title : G86M(0)-SSC	
ASUSTEK COMPUTER INC		Engineer: Shumin, Frank	
Size	Project Name	Date	Rev
Custom	ATS	18.18.2007	1.0
Date: 18.18.2007		Sheet: 31 of 88	

« Kennedy_Zhang »




PLACE ESD Diodes near CON3201



SN:12G101102152

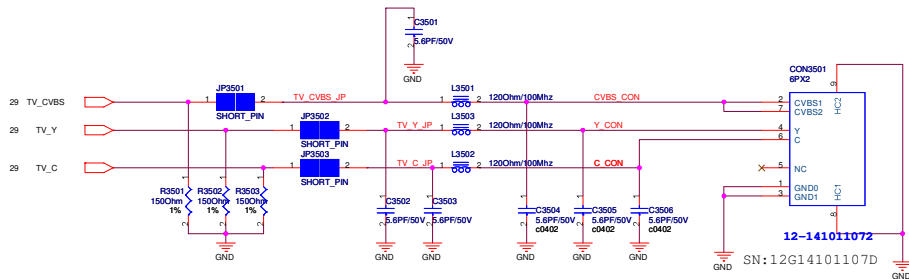
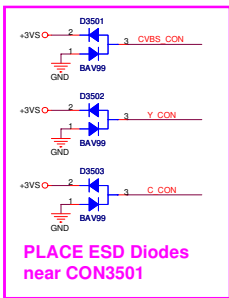
ASUS		Title : CRT	
ASUSTek COMPUTER INC			
Site		Project Name	
Custom	A7S	Engineer: Shunmin, Frank	
Date: 10/18/2007	Sheet 32	of 85	

<< Kennedy_Zhang >>

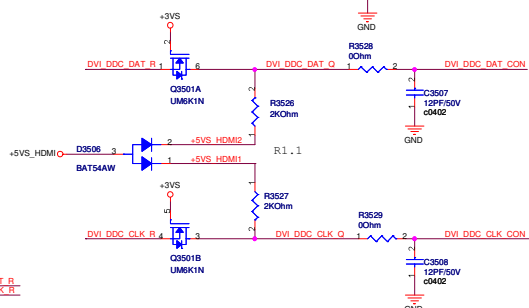
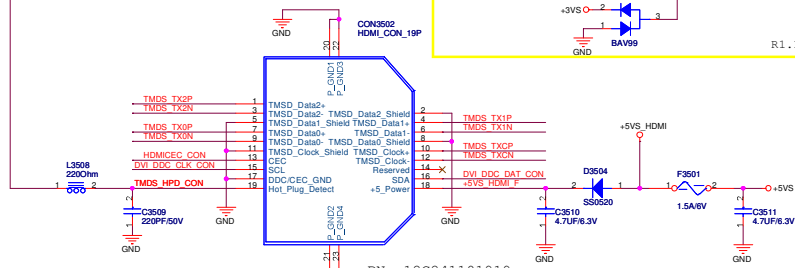
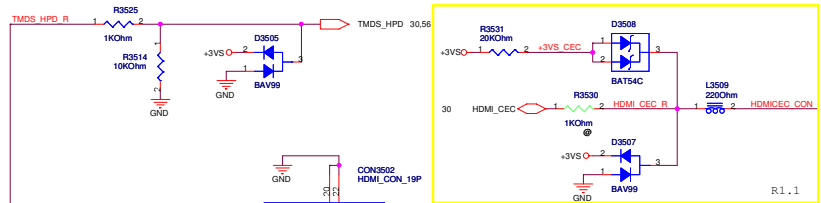
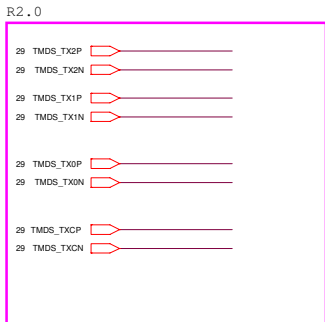
		Title : Schematic page name	
ASUSTEK COMPUTER INC		Engineer: Shumin, Frank	
Site	Project Name	Rev	
Custom	ATS	1.0	
Date: 8/18/2007		Sheet: 34	of 88

« Kennedy_Zhang »

TV OUT




HDMI



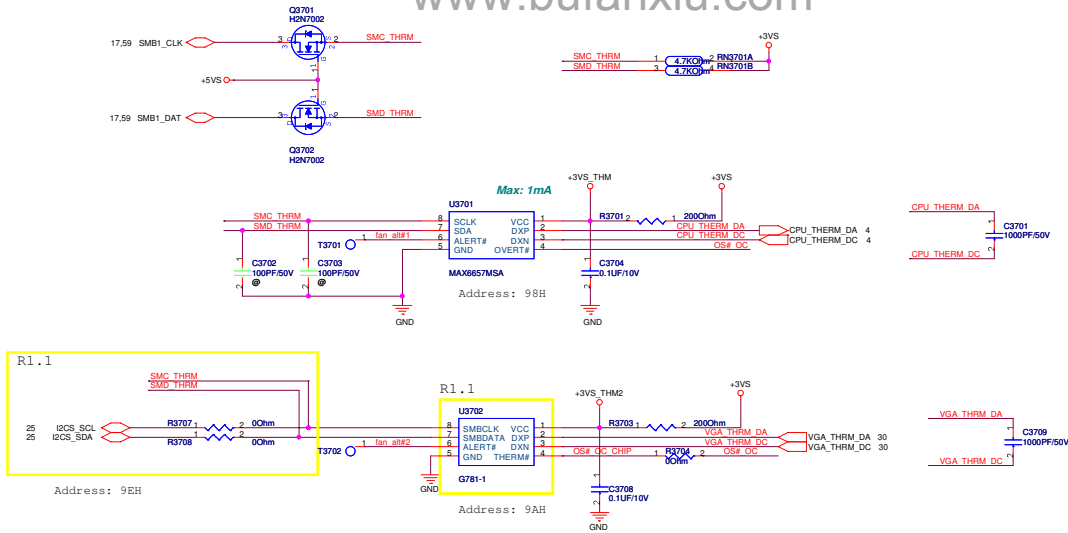
ASUS		Title : TV OUT & DVI CON.	
ASUSTEK COMPUTER INC		Engineer: Shumin, Frank	
Size	Project Name	Rev	
Custom	ATS	1.0	
Date: 8/18/2007	Sheet 35 of 35		

« Kennedy_Zhang »

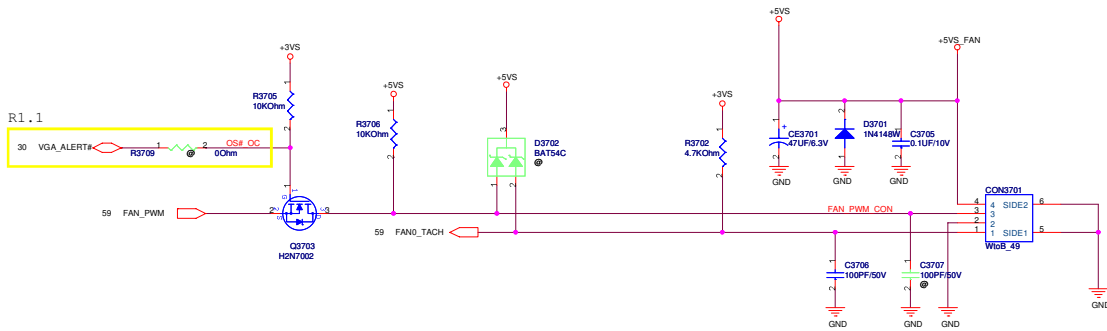
		Title : Schematic page name	
ASUSTEK COMPUTER INC		Engineer: Shumin, Frank	
Site	Project Name		Rev
Custom	ATS		1.0
Date: 8/18/2007		Sheet: 36	of 88

« Kennedy_Zhang »

Thermal Sensor




DC FAN Control

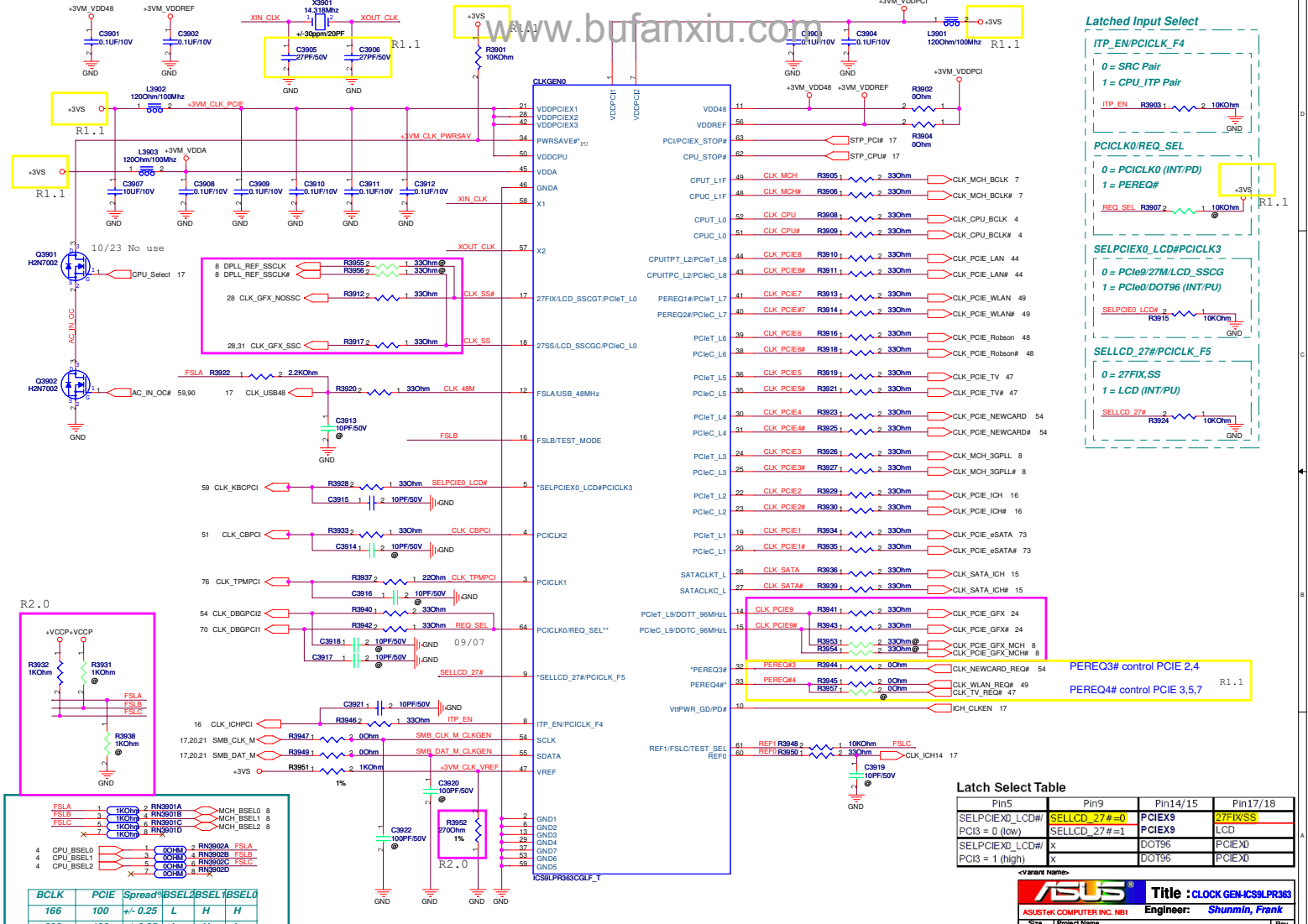


ASUS		Title : THER SENSOR & FAN	
ASUSTeK COMPUTER INC. NBI		Engineer: Shumin, Frank	
Size	Project Name:	Rev	
Custom	ATS	1.0	
Date: 8/18/2007		Sheet	37 of 88

« Kennedy_Zhang »

		Title : Schematic page name	
ASUSTEK COMPUTER INC		Engineer: Shumin, Frank	
Site	Project Name		Rev
Custom	ATS		1.0
Date: 8/18/2007		Sheet: 38	of 88

« Kennedy_Zhang »



Latched Input Select

ITP_EN#PCICLK_F4
 0 = SRC Pair
 1 = CPU_I/P Pair
 ITP_EN R3903.1 2 10KOhm

PCICLK0/REQ_SEL
 0 = PCICLK0 (INT/PPD)
 1 = PEREQ#
 REQ_SEL R3907.2 1 10KOhm

SELPCIE0_LCD#PCICLK3
 0 = PCIe@27M/LCD_SSCG
 1 = PCIe@DOT96 (INT/PU)
 SELPCIED LCD# R3915 1 10KOhm

SELLCD_27#PCICLK_F5
 0 = 27FX,SS
 1 = LCD (INT/PU)
 SELCD 27# R3924 1 10KOhm

Latch Select Table

	Pin5	Pin9	Pin14/15	Pin17/18
SELPCIE0_LCD#	SELLCD_27#=#0	PCIE#8	27FX/SS	
PCIE3 = 0 (low)	SELLCD_27#=#1	PCIE#9	LCD	
SELPCIE0_LCD#	x	DOT96	PCIE#0	
PCIE3 = 1 (high)	x	DOT96	PCIE#0	


ASUS
ASUSTeK COMPUTER INC. N61 Title : CLOCK GEN+CS9LPR383
 Engineer: Shunmin, Frank
 Size: Project Name: A7S
 Date: 11/18/2007 Sheet 38 of 98

« Kennedy_Zhang »

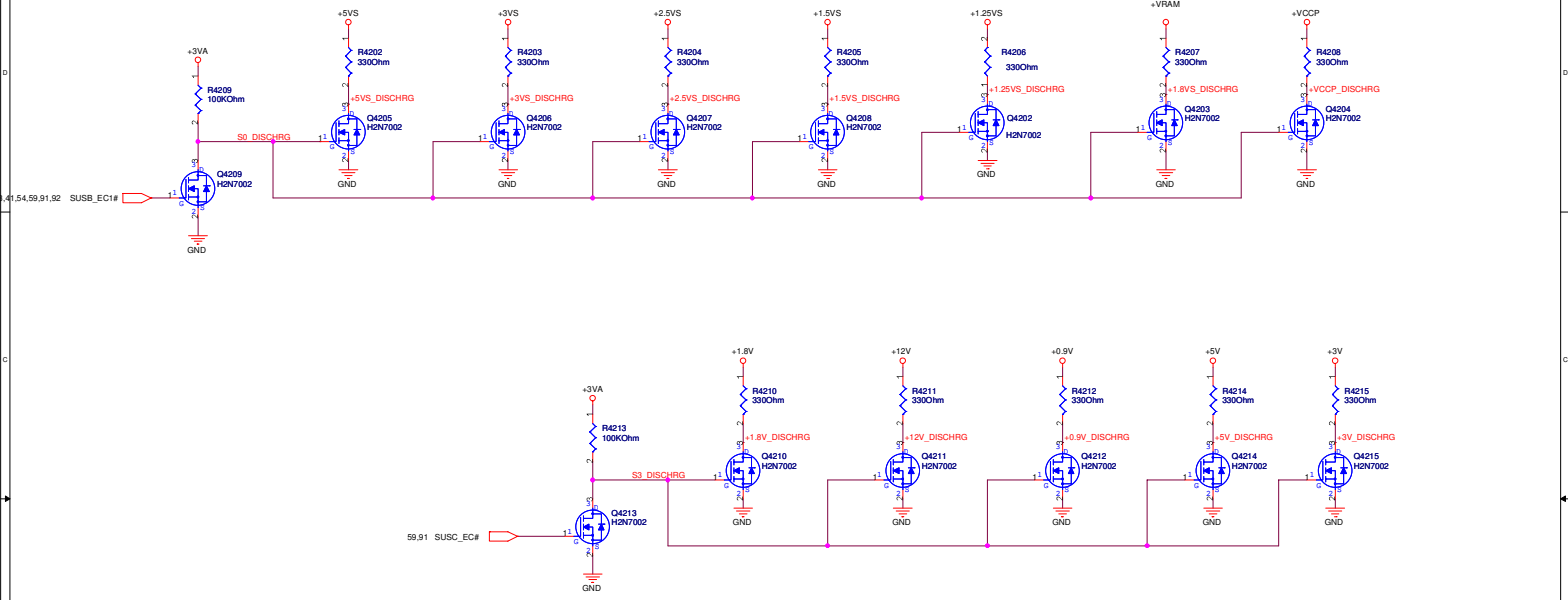
BCLK **PCIE** **Spread#** **BSEL2** **BSEL1** **BSEL0**

166	100	+/- 0.25	L	H	H
200	100	+/- 0.25	L	H	L

R1.1 Del LED

		Title : Schematic page name	
ASUSTEK COMPUTER INC		Engineer: Shumin, Frank	
Site	Project Name	Rev	
Custom	ATS	1.0	
Date: 8/18/2007	Sheet 40 of 88		


« Kennedy_Zhang »



Remove VM discharge
R1.1

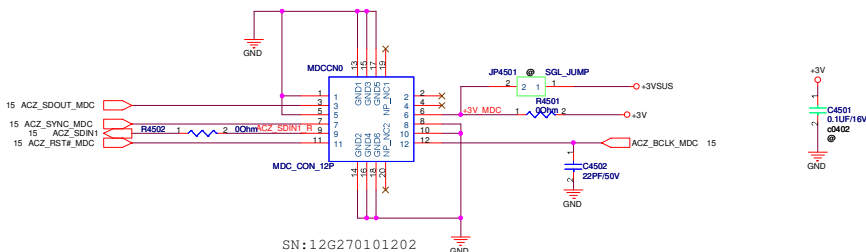
« Kennedy_Zhang »

ASUS		Title : DISCHARGE & EMI CAP	
ASUSTek COMPUTER INC. NB1		Engineer: Shunmin, Frank	
Size	Project Name	Rev	
Custom	A7S	1.0	
Date: 8/18/2007		Sheet: 42	of 88

		Title : Schematic page name	
ASUSTEK COMPUTER INC		Engineer: Shumin, Frank	
Site	Project Name		Rev
Custom	ATS		1.0
Date: 8/18/2007		Sheet: 43	of 88

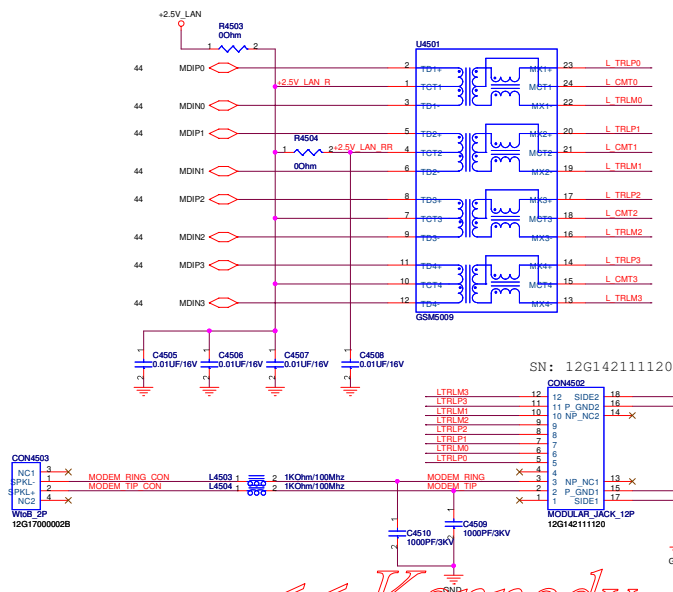
« Kennedy_Zhang »

MDC CONN.



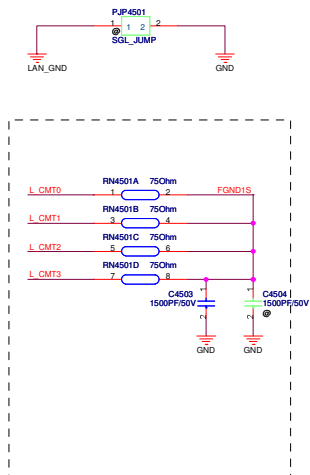
SN:12G270101202

LAN CONN.




SN: 12G142111120

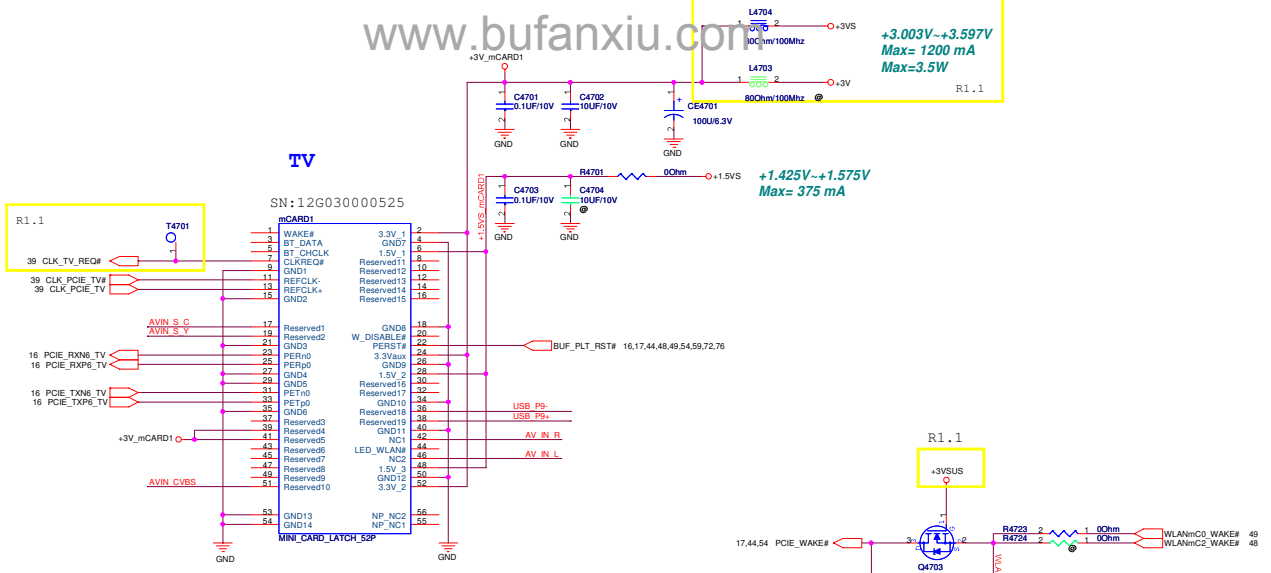
EMI need short 03/01



« Kennedy_Zhang »

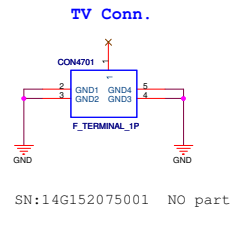
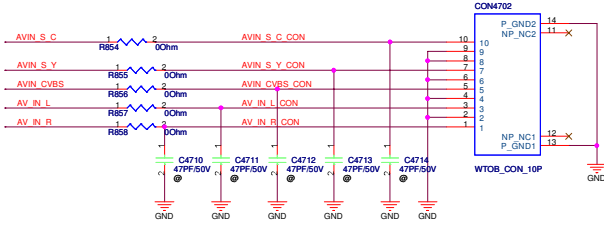
		Title : Schematic page name	
ASUSTEK COMPUTER INC		Engineer: Shumin, Frank	
Site	Project Name	Rev	
Custom	ATS	1.0	
Date: 8/18/2007	Sheet 46 of 88		

« Kennedy_Zhang »



Put it the middle the board

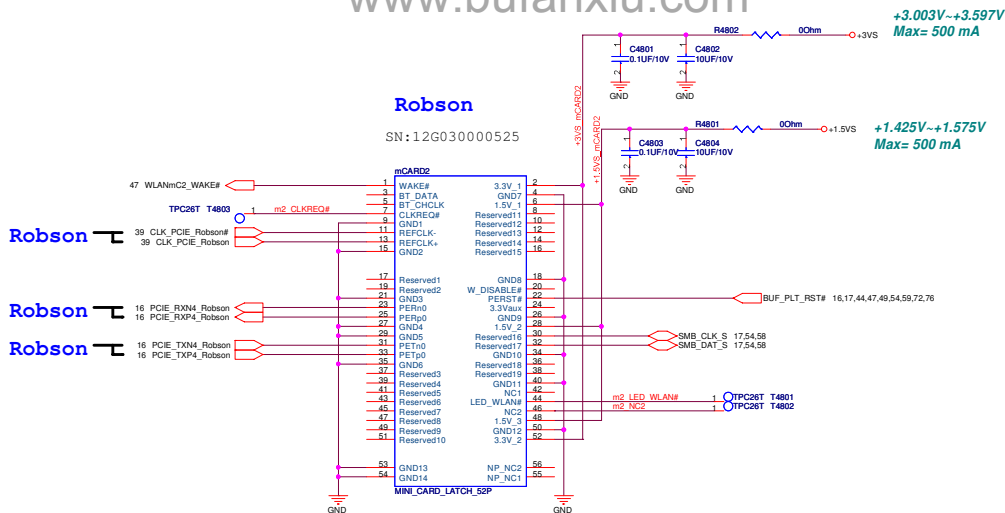
BOM ----> 12G171030104



SN:14G152075001 NO part

ASUS		Title: MINI CARD-TV/Windigo	
ASUSTek COMPUTER INC. NBI		Engineer: Shumin, Frank	
Size: Custom	Project Name: A7S	Date: 8/8/2012	Sheet: 47 of 88

« Kennedy_Zhang »

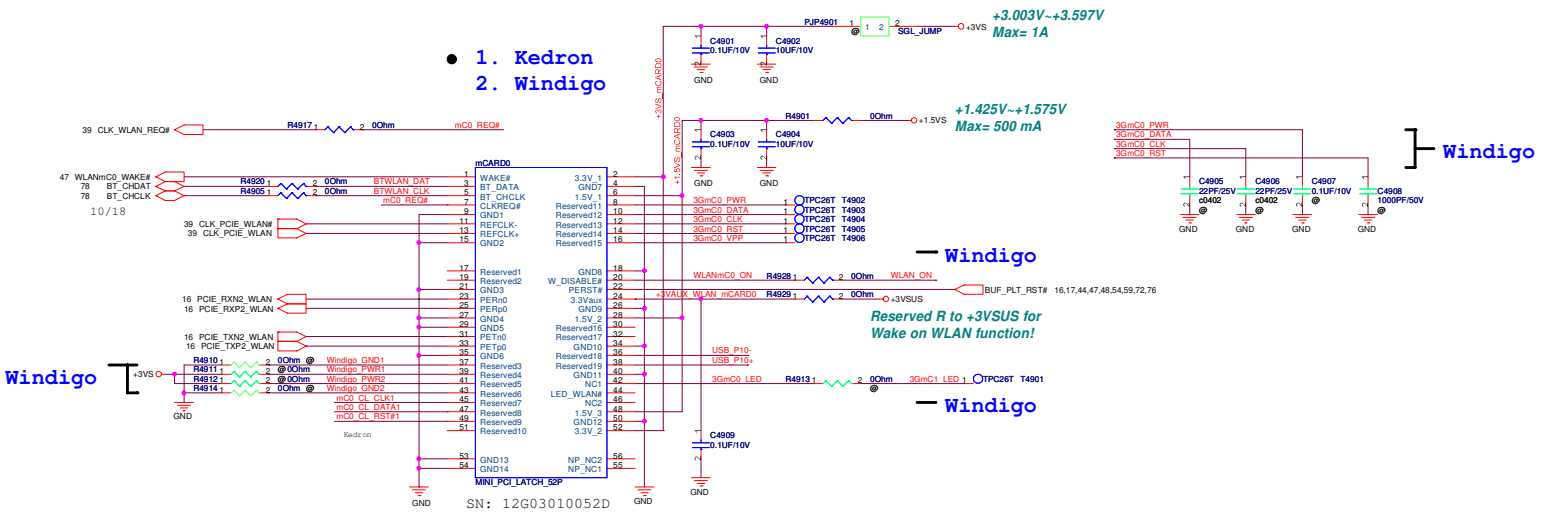


Put it down on the board

« Kennedy_Zhang »

ASUS		Title : MINI CARD-Robson	
ASUSTek COMPUTER INC. NBI		Engineer: Shumin, Frank	
Size	Project Name		Rev
Custom	ATS		1.0
Date: 8/11/2007		Sheet: 48	of 88

- 1. Kedron
- 2. Windigo



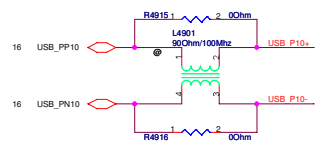
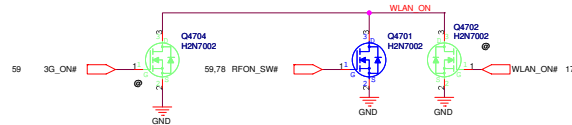
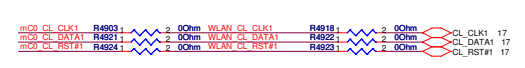
Windigo

Windigo

Reserved R to +3VSUS for Wake on WLAN function!


Windigo

Kedron

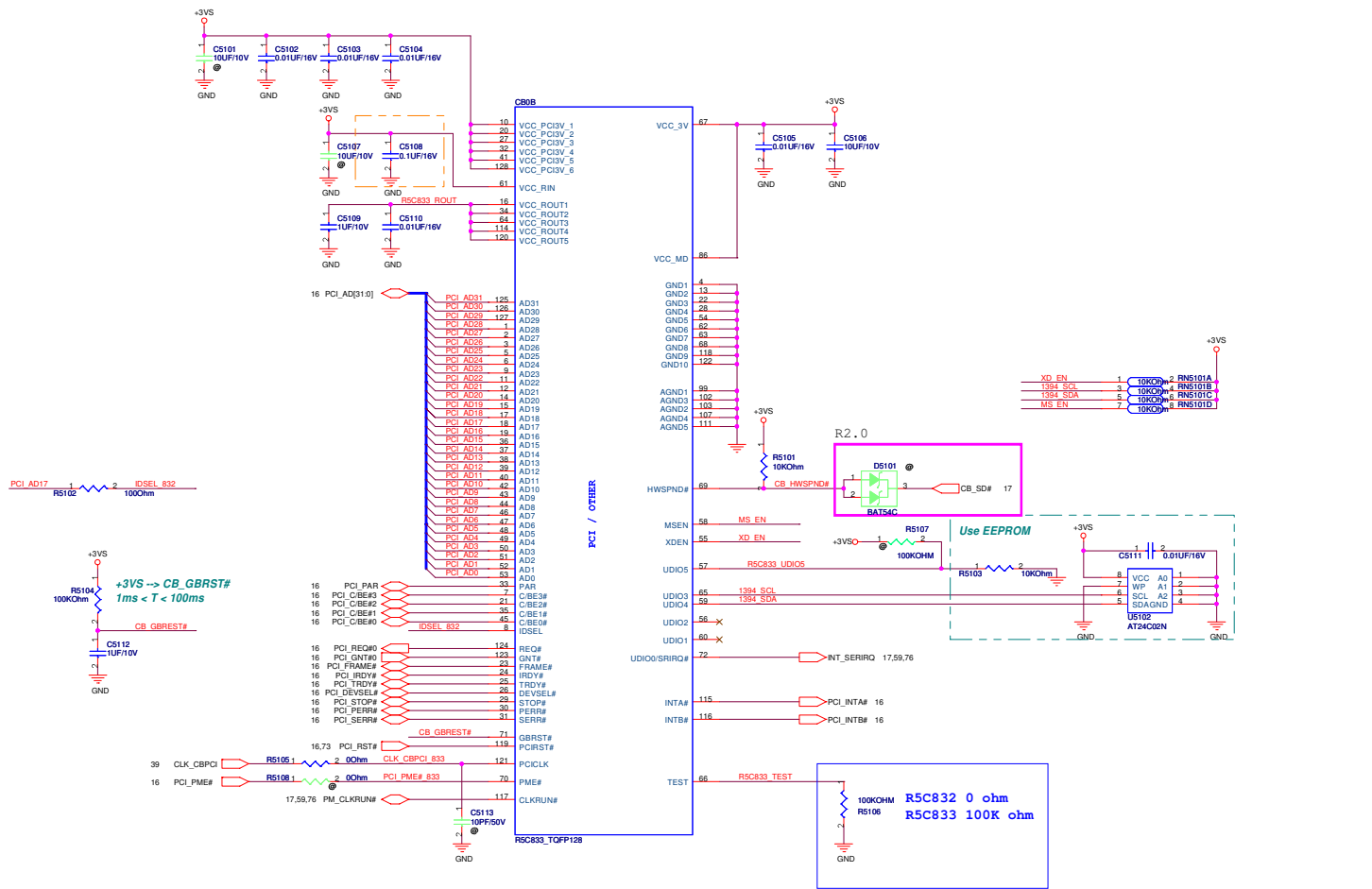


ASUS		Title : MINI_CARD_Kedron	
ASUSTEK COMPUTER INC. NBI		Engineer: Shumin, Frank	
Site	Project Name	Rev	
Custom	ATS	1.0	
Date: 8/20/2007		Sheet: 49	of 88

« Kennedy_Zhang »

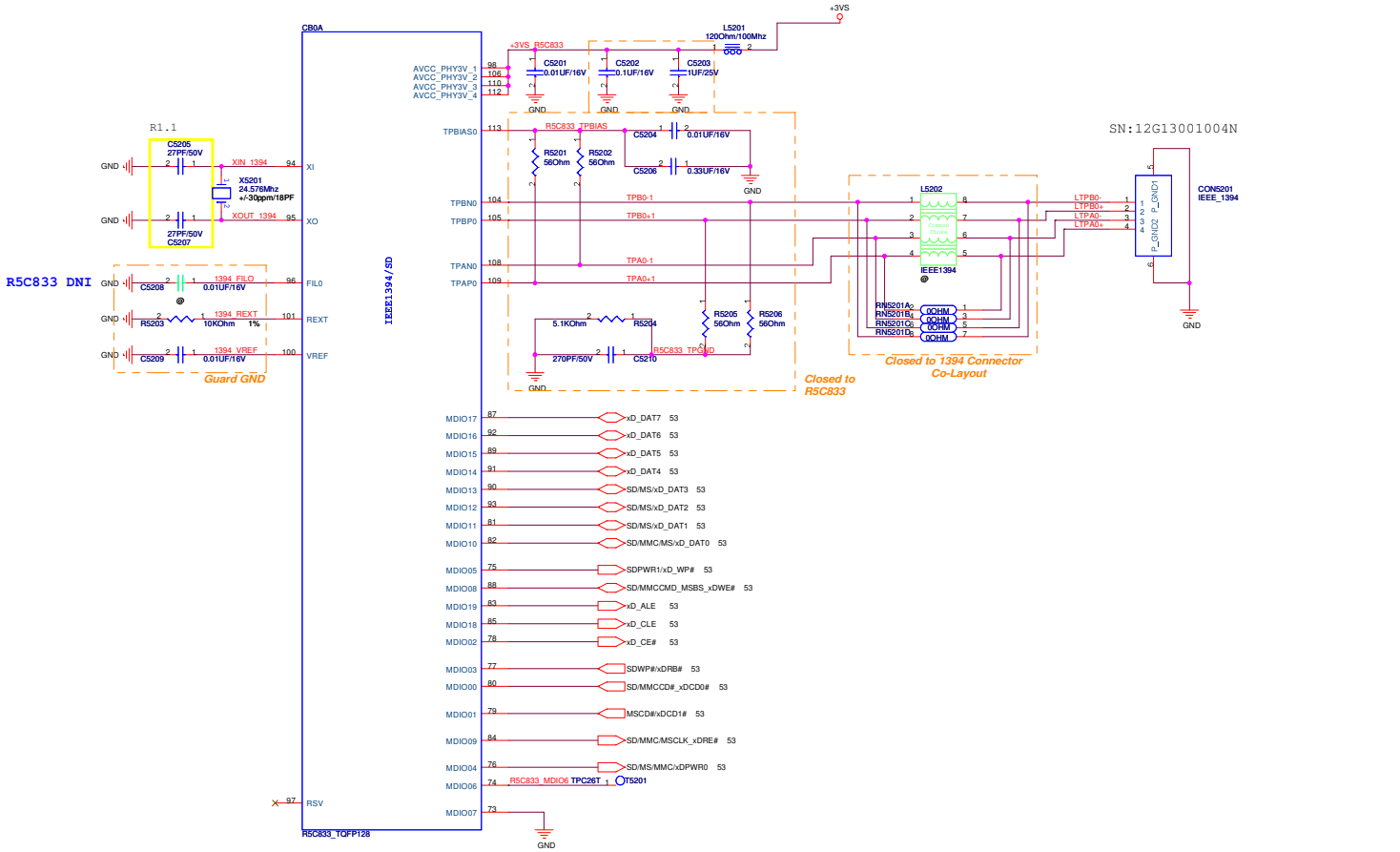
		Title : Schematic page name	
ASUSTEK COMPUTER INC		Engineer: Shumin, Frank	
Size	Project Name		Rev
Custom	ATS		1.0
Date: 8/18/2007		Sheet: 50	of 55

« Kennedy_Zhang »



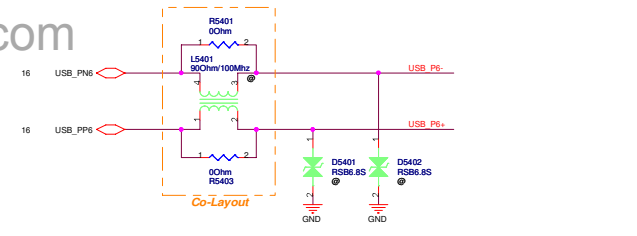
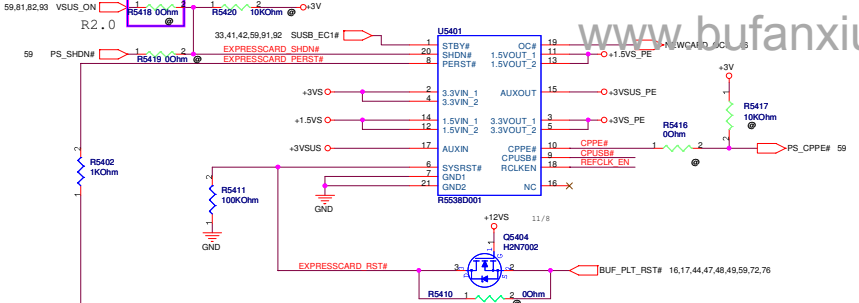
« Kennedy_Zhang »

ASUS		Title : CARD1394-R5C833(1)	
ASUSTEK COMPUTER INC. NBI		Engineer: Shumin, Frank	
Size	Project Name	Date	Rev
Custom	ATS	11-20-2007	1.0
Date: 11-20-2007		Sheet: 51 of 55	

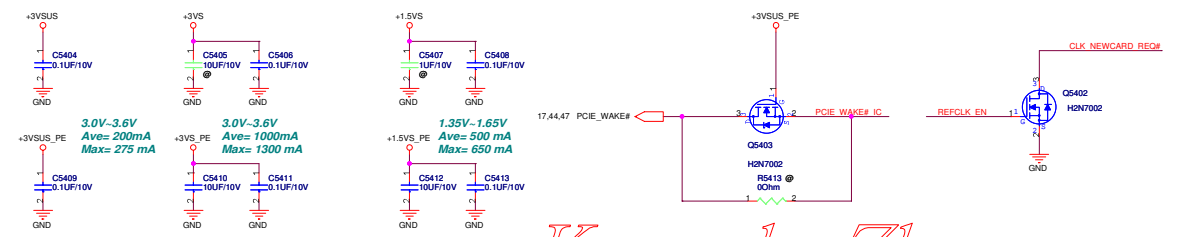
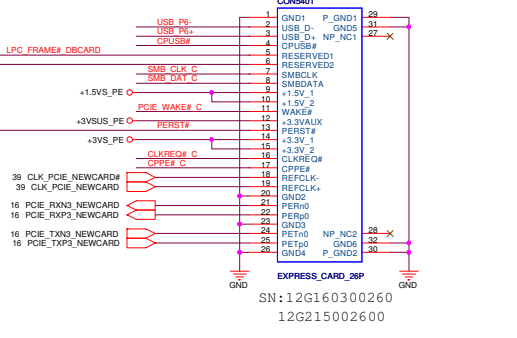
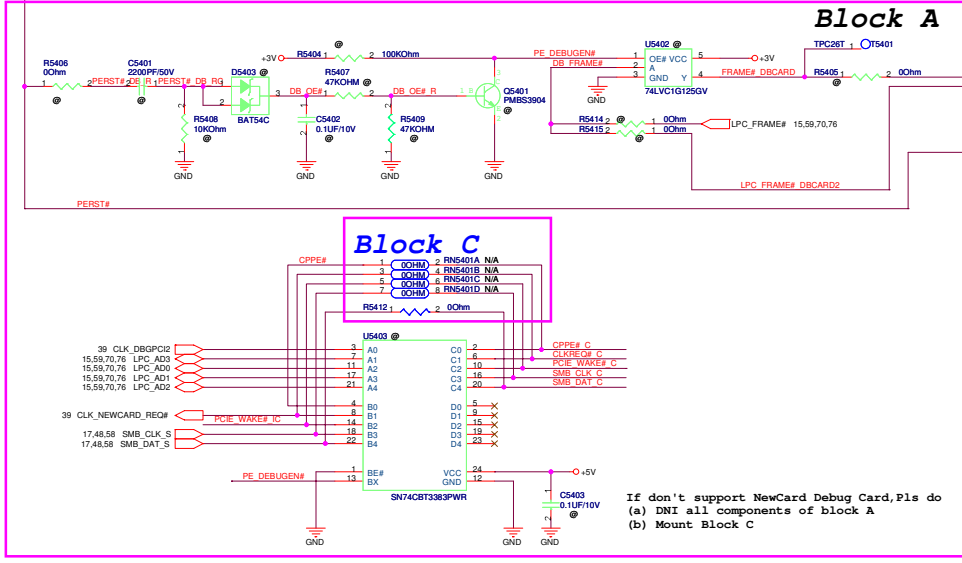


« Kennedy_Zhang »

ASUS		Title : CARD1394-R5C833(2)	
ASUSTEK COMPUTER INC. NBI		Engineer: Shumin, Frank	
Site	Project Name		Rev
Custom	ATS		1.0
Date: 8/18/2007		Sheet: 52	of 55




!! ExpressCard Standard 1.0:
Change Pin7 from RESERVED to SMBCLK
Change Pin8 from SMBCLK to SMBDATA
Change Pin9 from SMBDATA to +1.5V

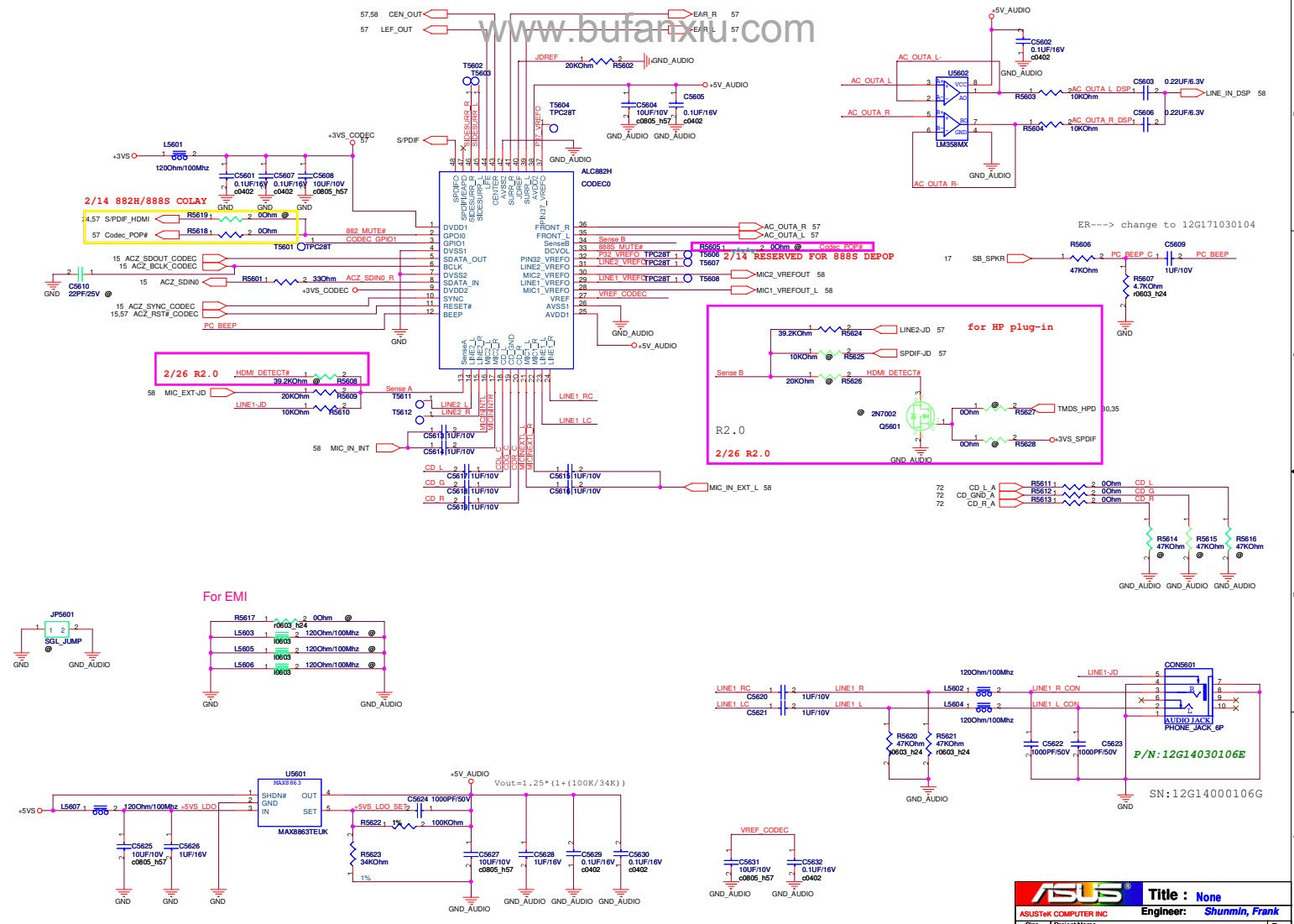


ASUS		Title : NEWCARD	
ASUSTek COMPUTER INC. (N1)		Engineer: Shumin, Frank	
Size	Project Name	Rev	
Custom	A7S	1.0	
Date: 8/8	18 2007	Sheet	54 of 88

« Kennedy_Zhang »

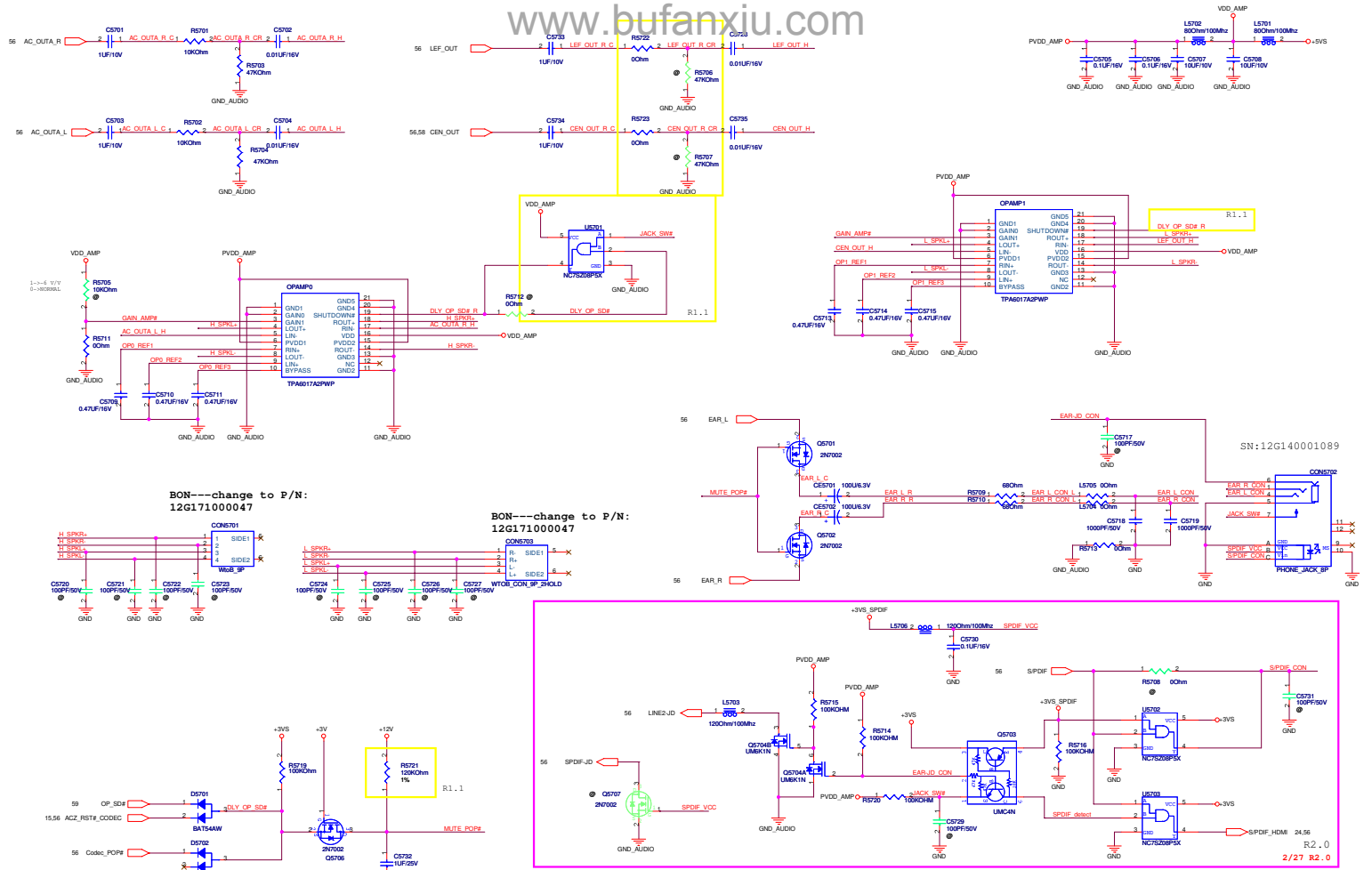
		Title : Schematic page name	
ASUSTeK COMPUTER INC. NB1		Engineer: Shumin, Frank	
Size	Project Name	Rev	
Custom	ATS	1.0	
Date: 8/18/2007	Sheet 55 of 88		

« Kennedy_Zhang »



« Kennedy_Zhang »

ASUS		Title : None
ASUSTEK COMPUTER INC		Engineer: Shumin, Frank
Site	Project Name	Rev
Custom	ATS	1.0
Date: 8/11/2007	Sheet: 56	of 55



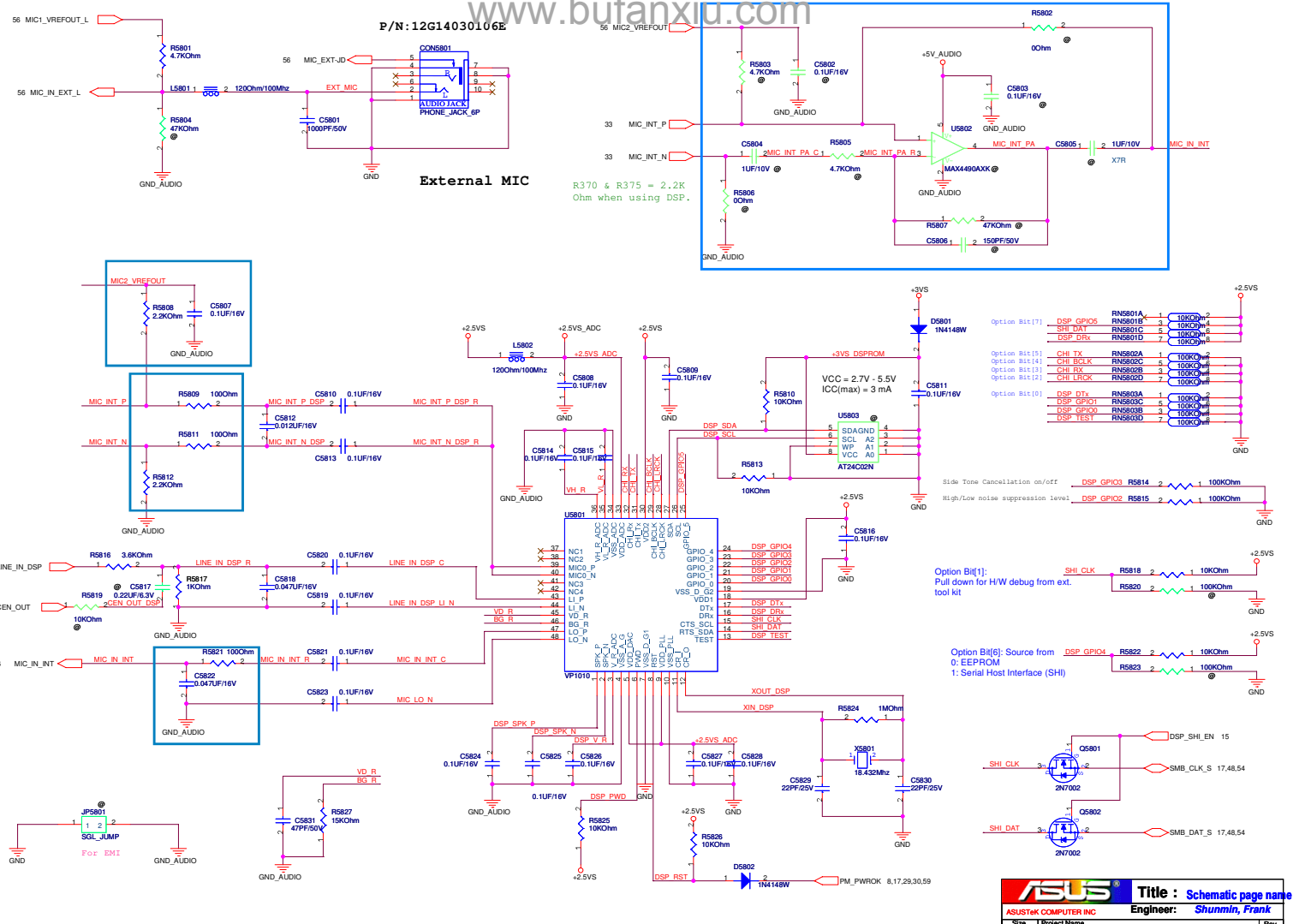
BON---change to P/N:
12G171000047

BON---change to P/N:
12G171000047

TYPE	LEVEL	LEVEL	LEVEL
EAR-JD	L	OPEN	OPEN
JACK_SWF	L	L	H
EAR_OUT	S/PDIF_OUT	OFF	OFF

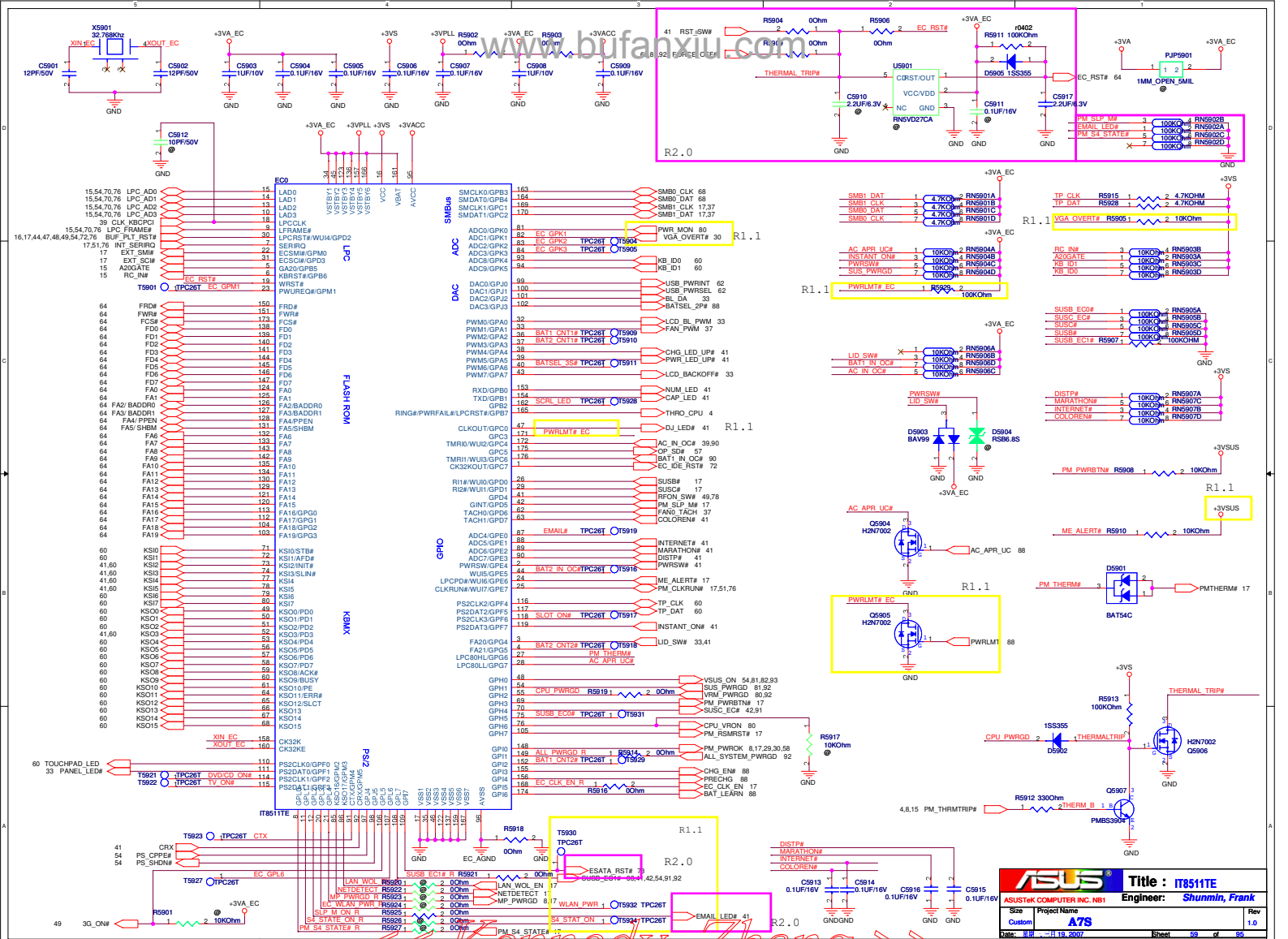
ASUS Title: Audio AMP
 ASUSTek COMPUTER INC. Engineer: Shunmin, Frank
 Size: Project Name:
 Current: A7S
 Date: 11/19/2007 Sheet 57 of 82

<< Kennedy_Zhang >>



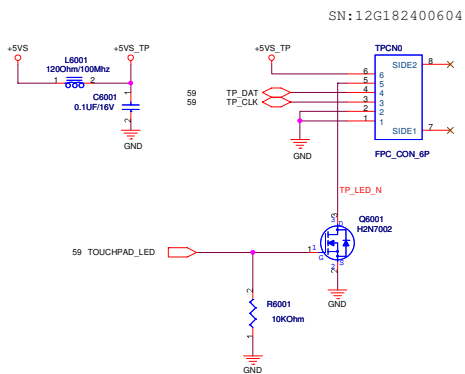
« Kennedy_Zhang »

ASUS		Title : Schematic page name
ASUSTeK COMPUTER INC		Engineer: Shumin, Frank
Size	Project Name	Rev
Custom	ATS	1.0
Date: 8/18/2007	Sheet	68 of 68



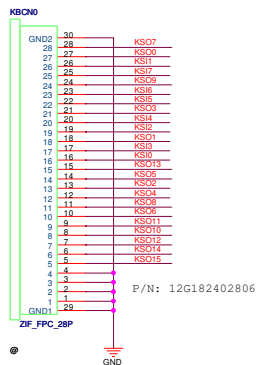
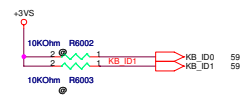
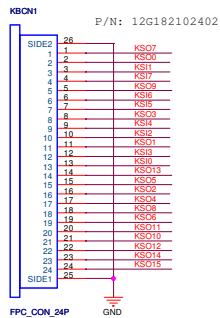
ASUS		Title : IT8511TE	
ASUSTeK COMPUTER INC. NBI		Engineer: Shumin, Frank	
Size	Project Name	Date	Rev
Custom	ATS	18.2007	1.0
		Sheet 89 of 89	

For Touch-Pad



For Keyboard

Big Keyboard for A7S



Small Keyboard for G2S

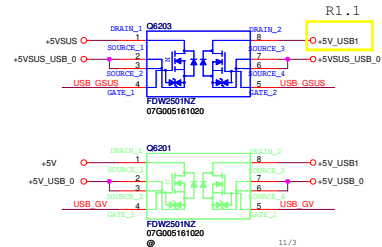
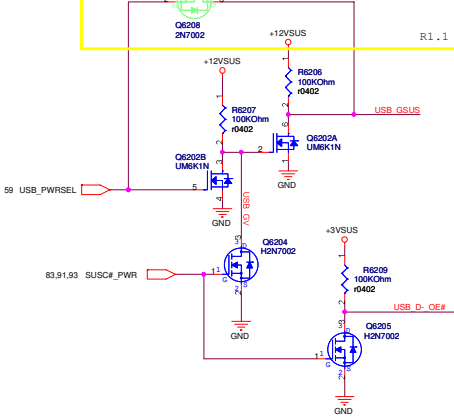
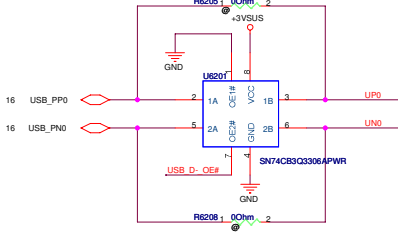
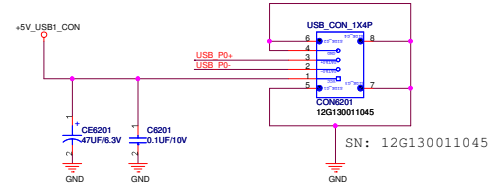
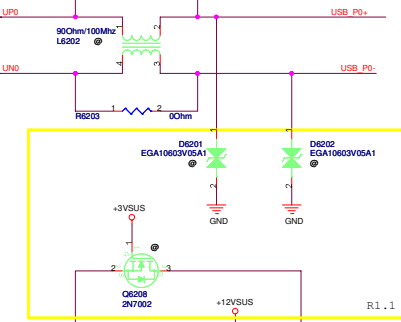
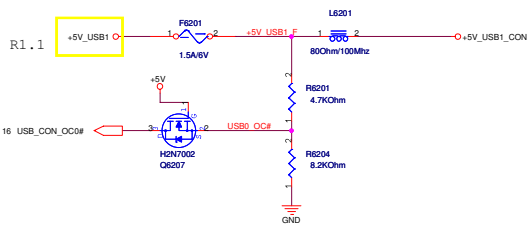
ASUS		Title : Touch Pad & KB	
ASUSTEK COMPUTER INC. NEI		Engineer: Shumin, Frank	
Size	Project Name		Rev
Custom	A7S		1.0
Date: 8/8	18 2007	Sheet: 60	of 88

« Kennedy_Zhang »

Remove IAMT control
logic
R1.1

		Title: Sequence Control Logic	
ASUSTeK COMPUTER INC. NBI		Engineer: Shunmin, Frank	
Site	Project Name		Rev
Custom	ATS		1.0
Date:	8/18/2007	Sheet	01 of 05

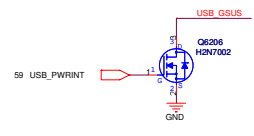
<< Kennedy_Zhang >>



USB_PWRSEL	SUSC#_PWRH	USB_GV	USB_GSUS
H	X	L	H
L	L	L	L

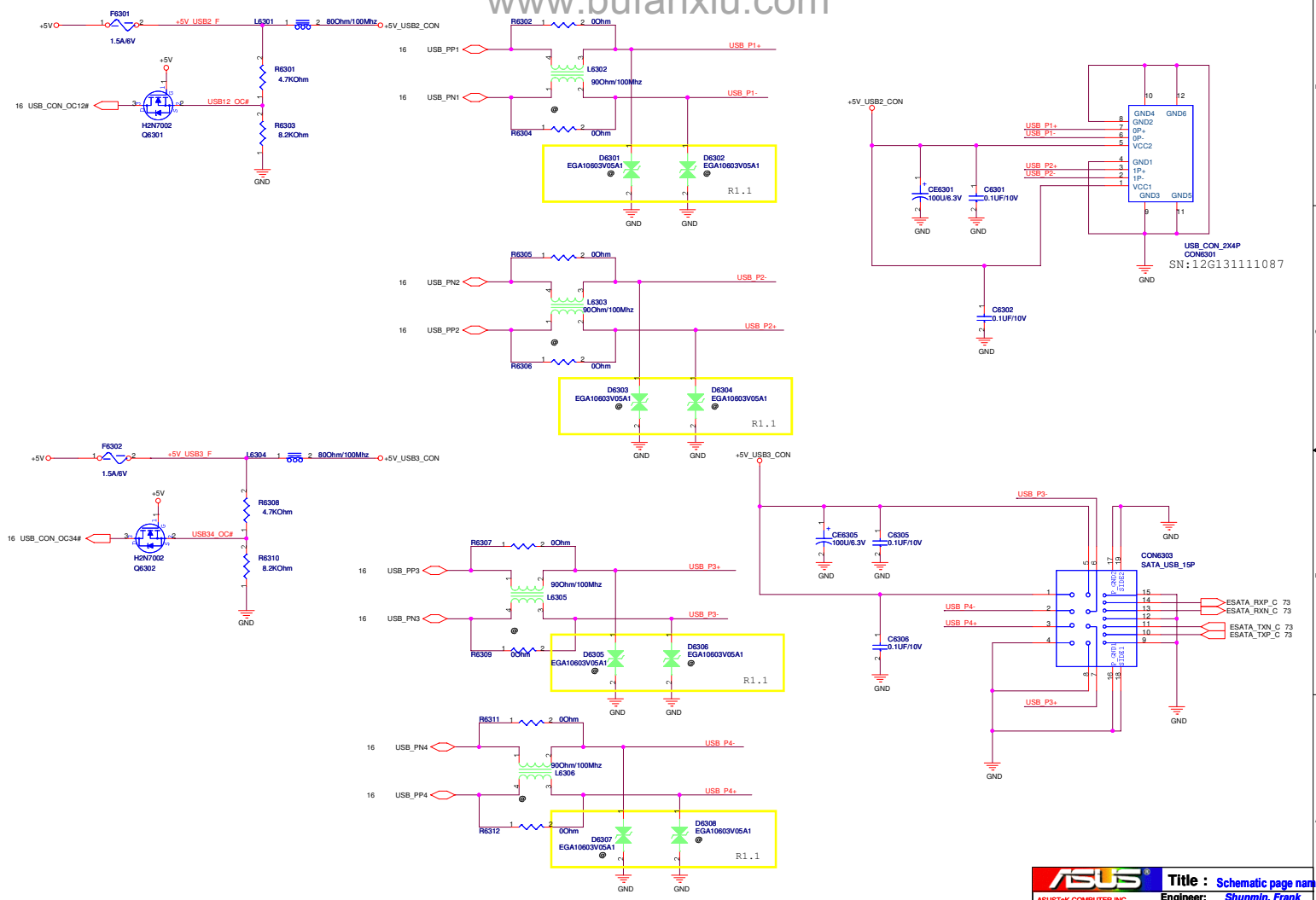
USB_PWRSEL
Select Enable (H)/ Disable (L)
USB Port Charging Function on S4/ S5

GPIO6
Select Enable (H)/ Disable (L)
USB Port Power Interrupt



ASUS		Title : USB CONN	
ASUSTEK COMPUTER INC		Engineer: Shumin, Frank	
Size	Project Name	Rev	
Custom	A7S	1.0	
Date: 11/18/2007		Sheet: 62	of 88

« Kennedy_Zhang »

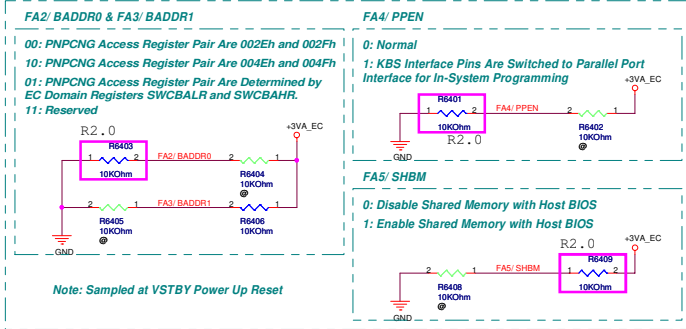


ASUS		Title : Schematic page name	
ASUSTek COMPUTER INC		Engineer: Shumin, Frank	
Size	Project Name		Rev
Custom	AT5		1.0
Date: 8/8	18 2007	Sheet	63 of 88

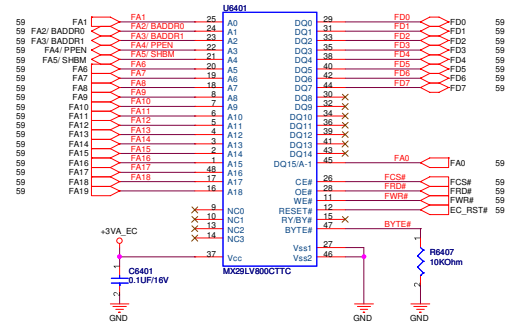
« Kennedy_Zhang »

ISA ROM

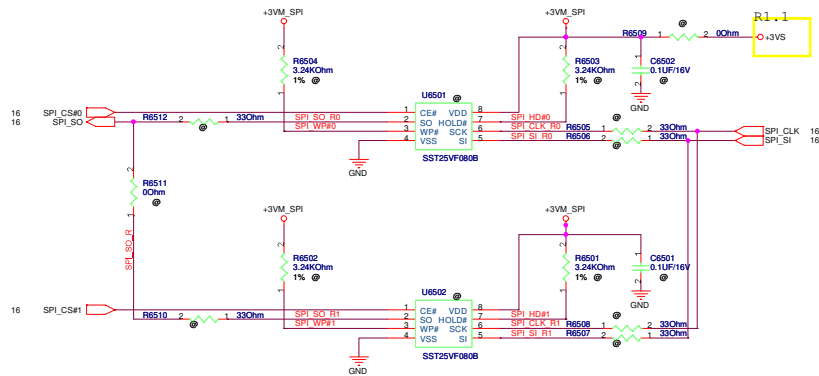
EC Hardware Strapping



8M TSOP




« Kennedy_Zhang »




ASUS		Title : SPI ROM	
ASUSTEK COMPUTER INC. NB1		Engineer: Shunmin, Frank	
Size	Project Name	Rev	
Custom	ATS	1.0	
Date: 8/18/2007		Sheet: 65	of 88

« Kennedy_Zhang »

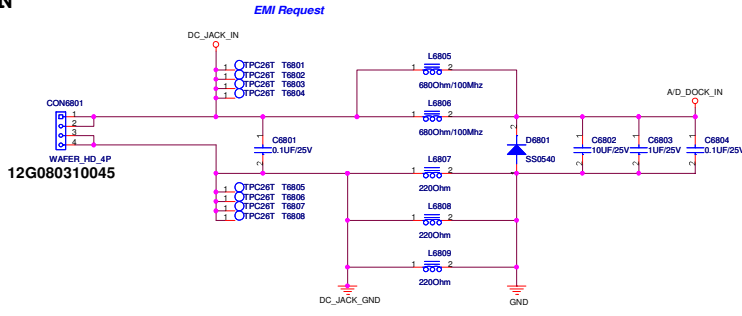
		Title : LED	
ASUSTEK COMPUTER INC		Engineer: <i>Shunmin, Frank</i>	
Site	Project Name		Rev
Custom	ATS		1.0
Date: 8/18/2007		Sheet: 66	of 88

<< Kennedy_Zhang >>

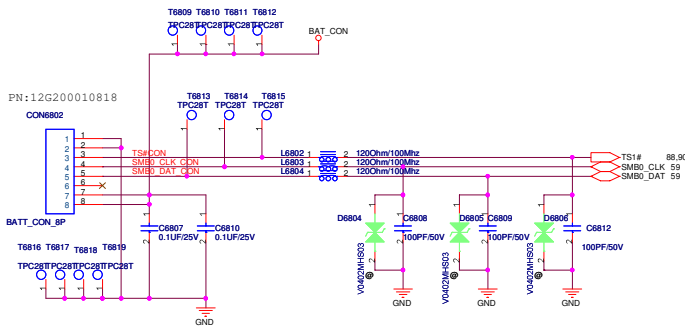
		Title : Schematic page name	
ASUSTEK COMPUTER INC		Engineer: Shumin, Frank	
Size	Project Name		Rev
Custom	ATS		1.0
Date: 8/18/2007		Sheet: 67	of 88

« Kennedy_Zhang »

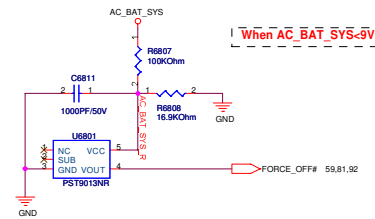
DC-IN



Battery Connector




Without Battery & Pull out Adapter



ASUS		Title : DC & BAT IN	
ASUSTEK COMPUTER INC. N61		Engineer: Shunmin, Frank	
Size	Project Name		Rev
Custom	ATS		1.0
Date: 8/19/2007		Sheet	68 of 88

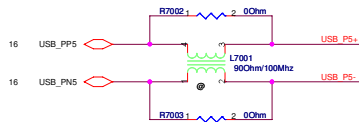
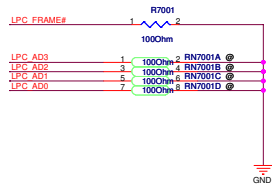
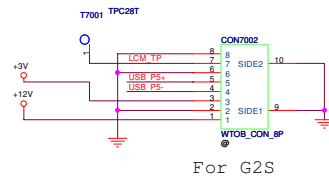
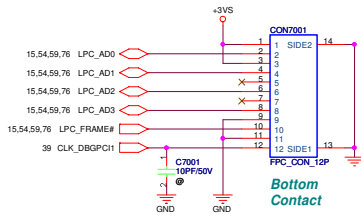
« Kennedy_Zhang »

		Title : Schematic page name	
ASUSTEK COMPUTER INC		Engineer: Shumin, Frank	
Site	Project Name	Rev	
Custom	ATS	1.0	
Date: 8/18/2007	Sheet 69 of 88		

« Kennedy_Zhang »


For Debug

If support NewCard Debug Card,
Pls don't mount all components.

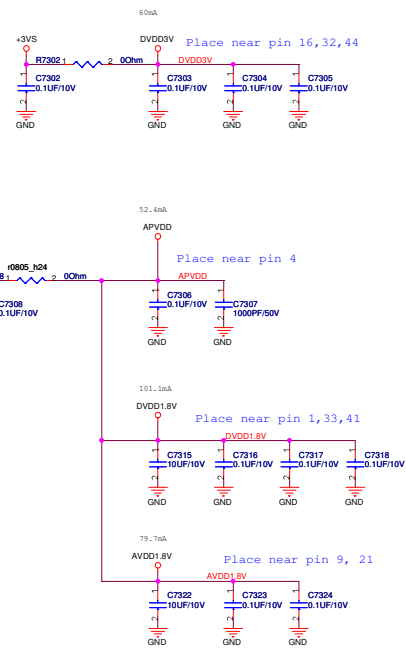
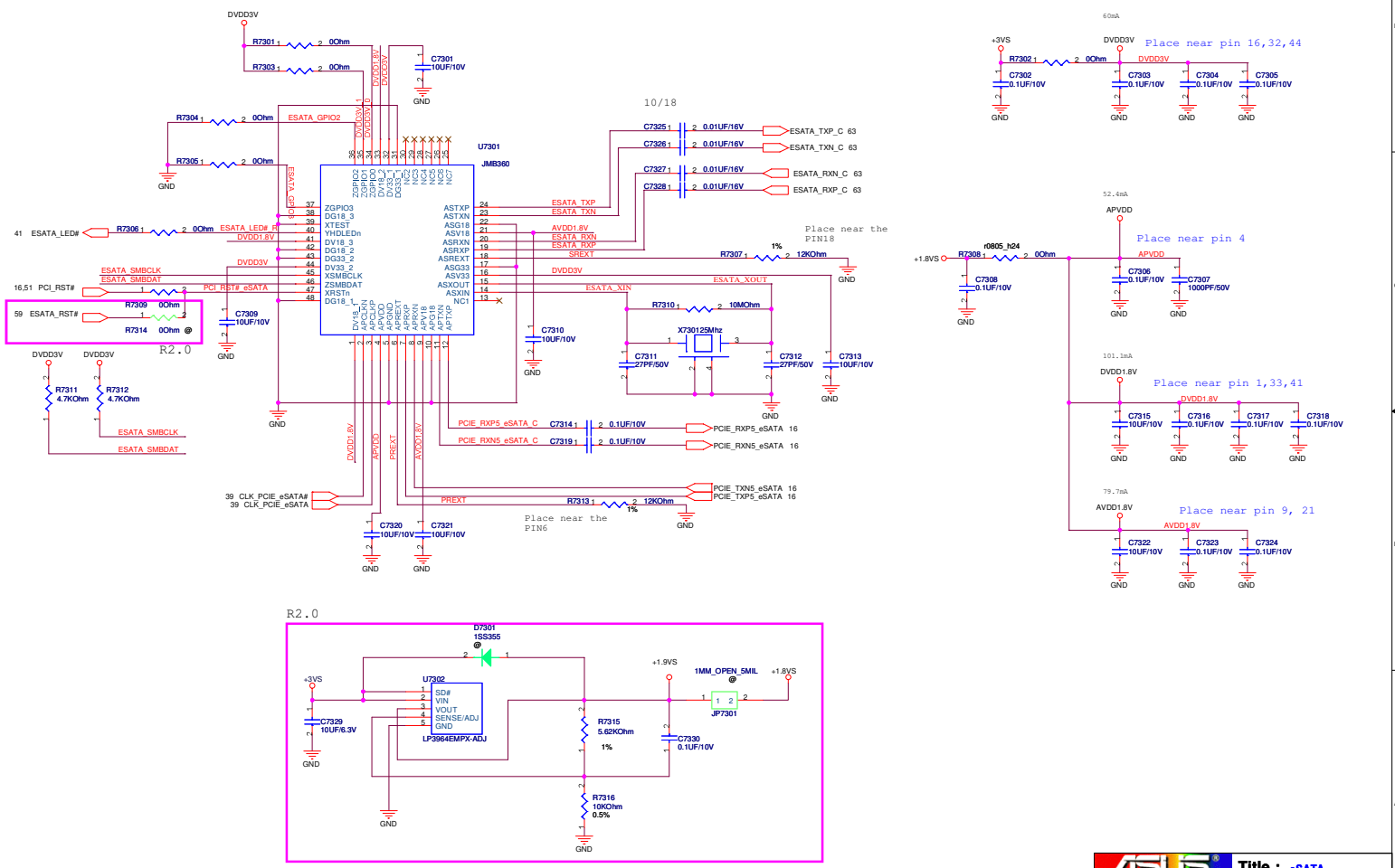


ASUS		Title : Debug CONN.	
ASUSTEK COMPUTER INC		Engineer: Shumin, Frank	
Size	Project Name		Rev
Custom	ATS		1.0
Date: 8/21/2007		Sheet 70	of 88

« Kennedy_Zhang »

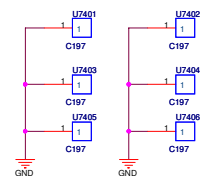
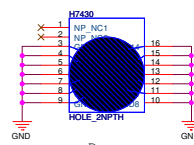
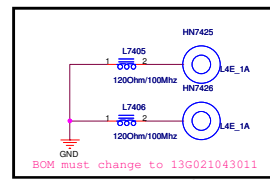
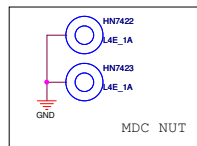
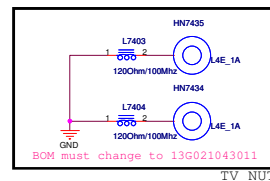
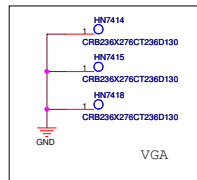
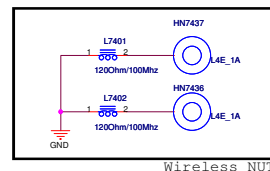
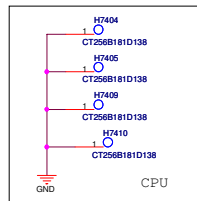
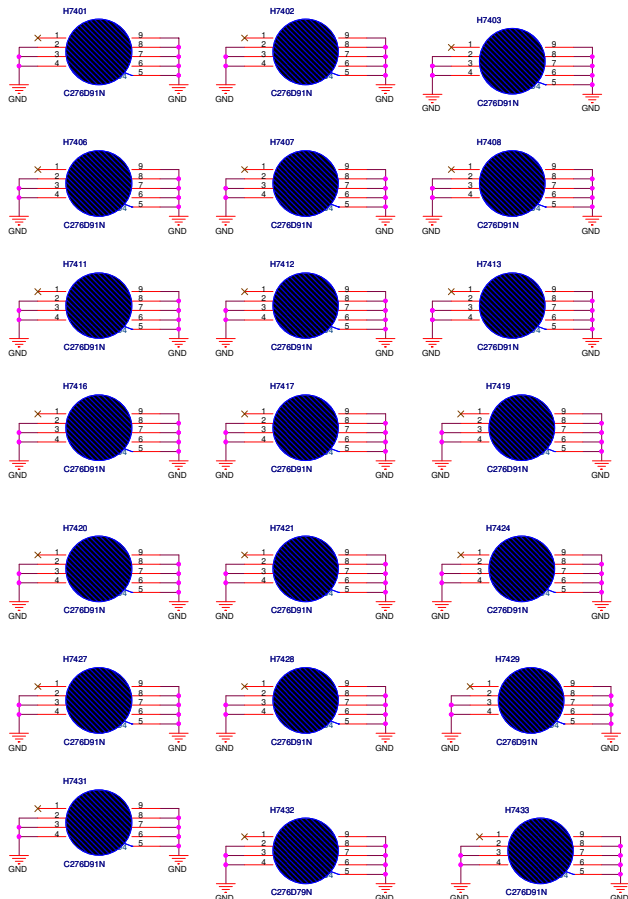
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ASUSTEK COMPUTER INC		Engineer: Shumin, Frank	
Site	Project Name	Rev	
Custom	ATS	1.0	
Date: 8/18/2007		Sheet	71 of 88

« Kennedy_Zhang »



ASUS		Title : eSATA	
ASUSTek COMPUTER INC. NBI		Engineer: Shumlin, Frank	
Size	Project Name	Client	Rev
Custom	ATS		1.0
Date: 8/11/2007			Sheet 73 of 88

« Kennedy_Zhang »




G

E

D

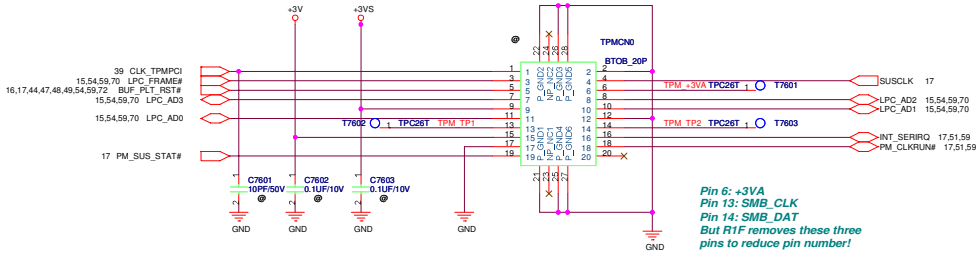
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ASUSTek COMPUTER INC		Engineer: Shunmin, Frank	
Size	Project Name		Rev
Custom	ATS		1.0
Date: 星期三, 三月 20, 2007		Sheet	74 of 95

« Kennedy_Zhang »

		Title : Schematic page name	
ASUSTEK COMPUTER INC		Engineer: Shumin, Frank	
Size	Project Name		Rev
Custom	ATS		1.0
Date: 8/18/2007		Sheet: 75	of 88

« Kennedy_Zhang »

For TPM Module



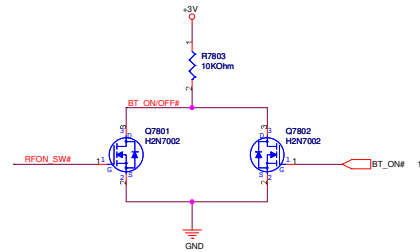
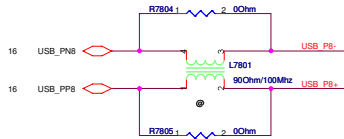
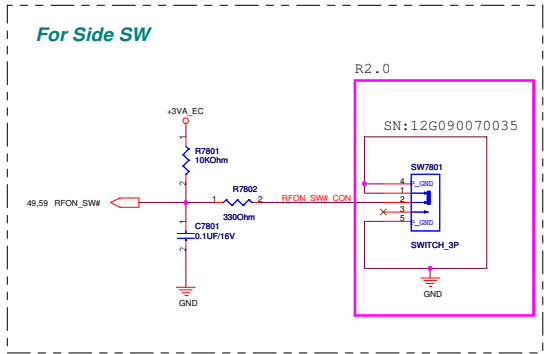
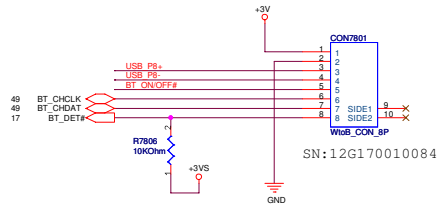
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ASUSTEK COMPUTER INC		Engineer: Shunmin, Frank	
Site	Project Name	Rev	
Custom	ATS	1.0	
Date: 8/18/2007	Sheet: 76		of 88

« Kennedy_Zhang »

		Title: thematic page name	
ASUSTEK COMPUTER INC		Engineer: Shumin, Frank	
Size	Project Name		Rev
Custom	ATS		1.0
Date: 8/8/2007		Sheet	77 of 88

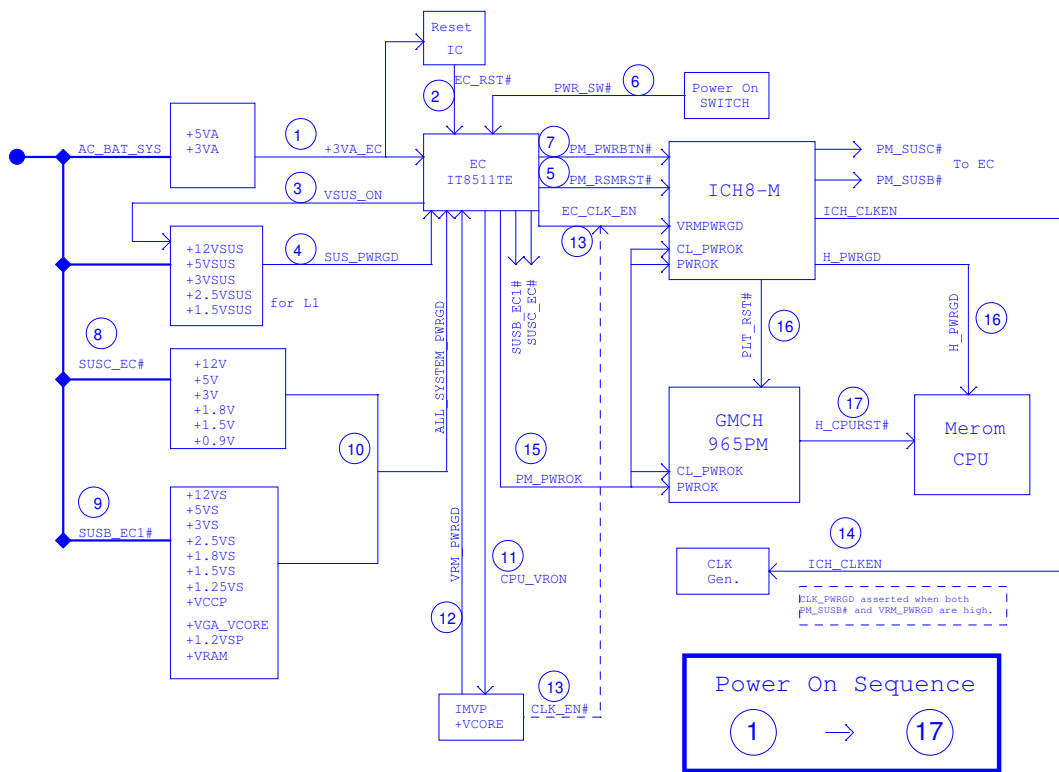
« Kennedy_Zhang »

For Bluetooth

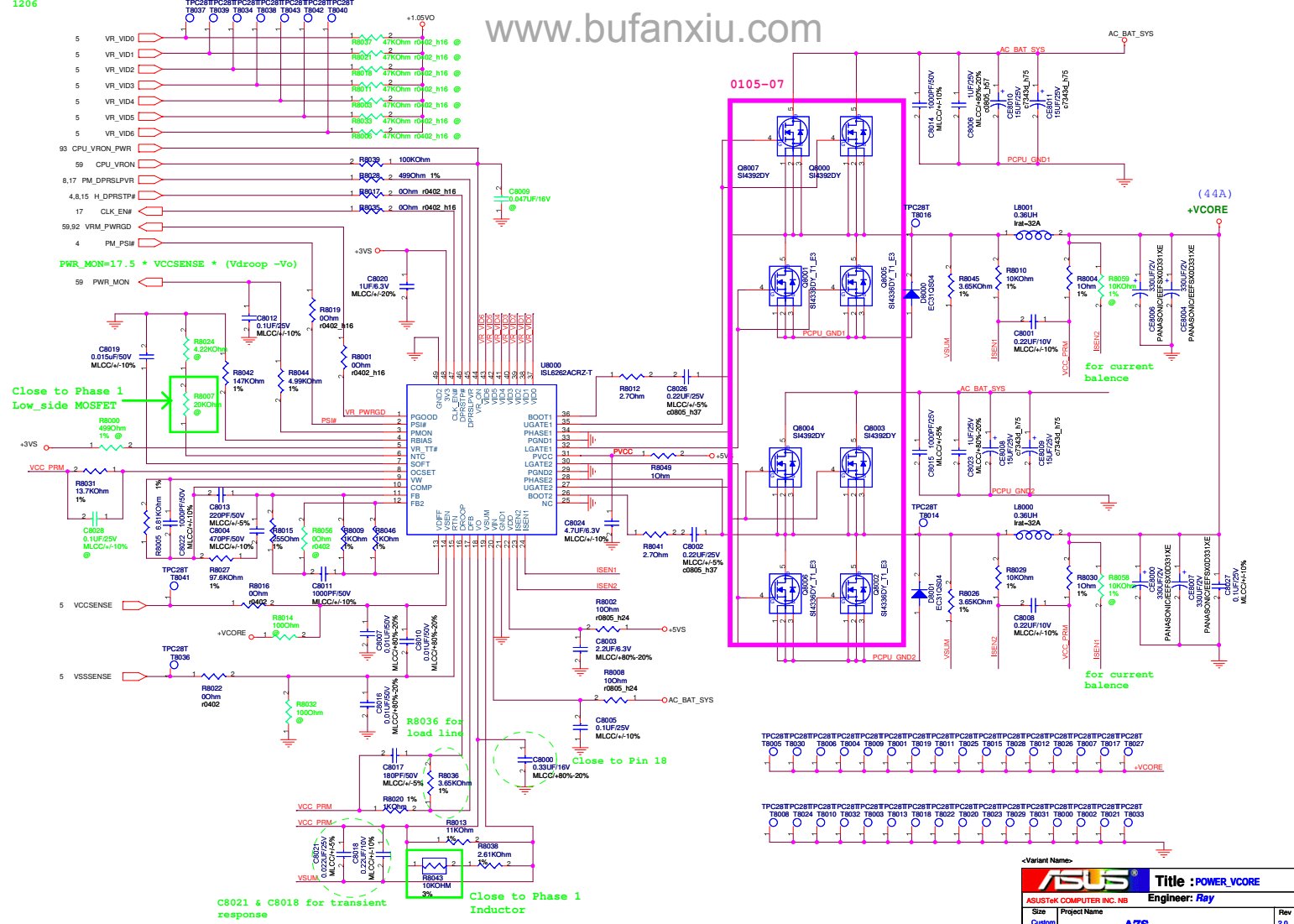


ASUS		Title : Blue Tooth	
ASUSTek COMPUTER INC		Engineer: Shumin, Frank	
Site	Project Name	Rev	
Custom	ATS	1.0	
Date: 8/18/2007		Sheet: 78	of 88

« Kennedy_Zhang »



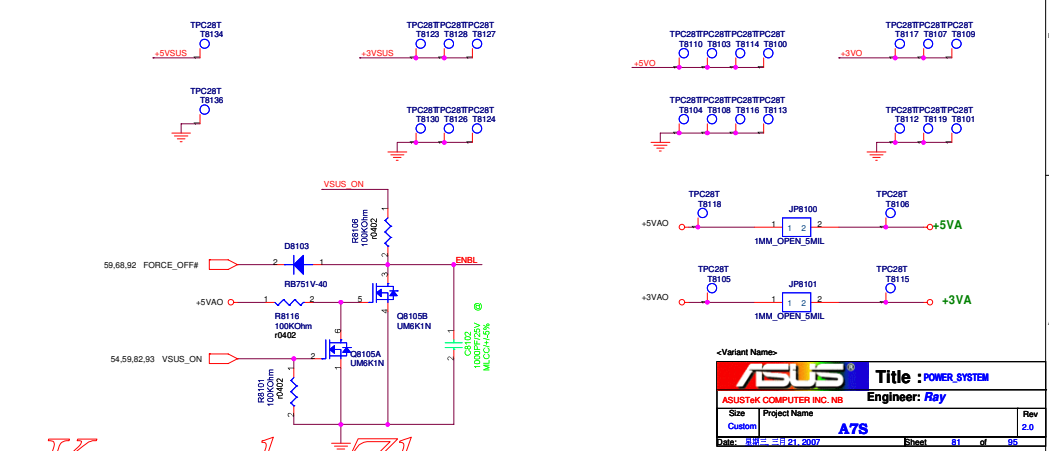
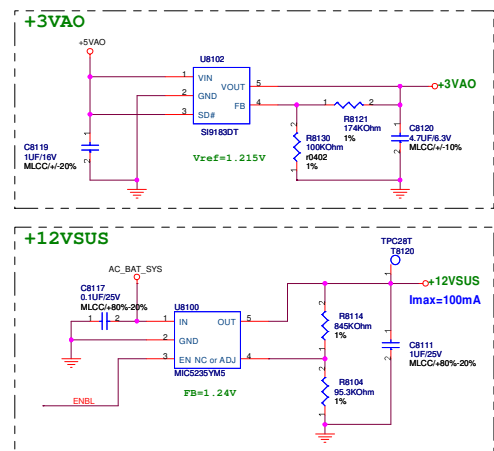
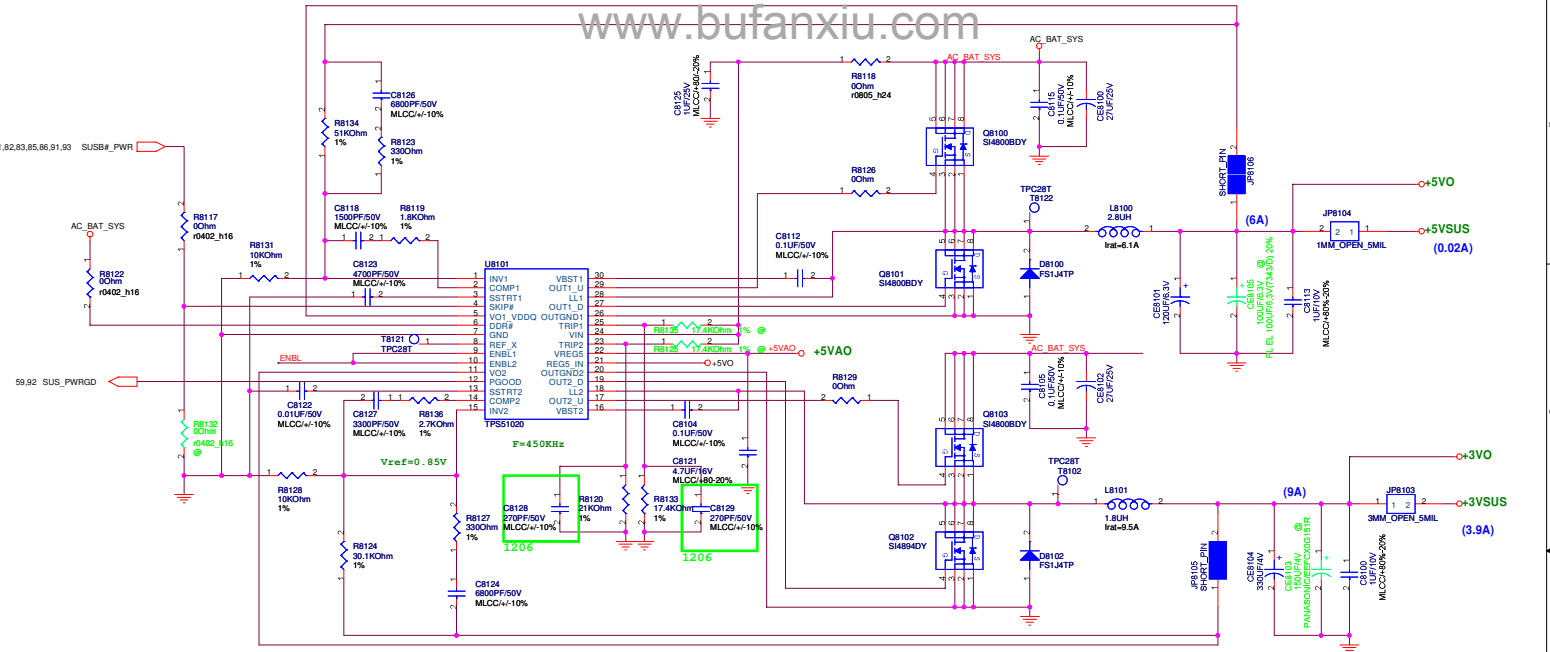
« Kennedy_Zhang »



<Variant Name>

ASUS		Title : POWER_VCORE
ASUSTEK COMPUTER INC. NB	Engineer: Ray	
Size	Project Name	Rev
Custom	A7S	2.0
Date: 2007-08-21	Sheet: 80	of 88

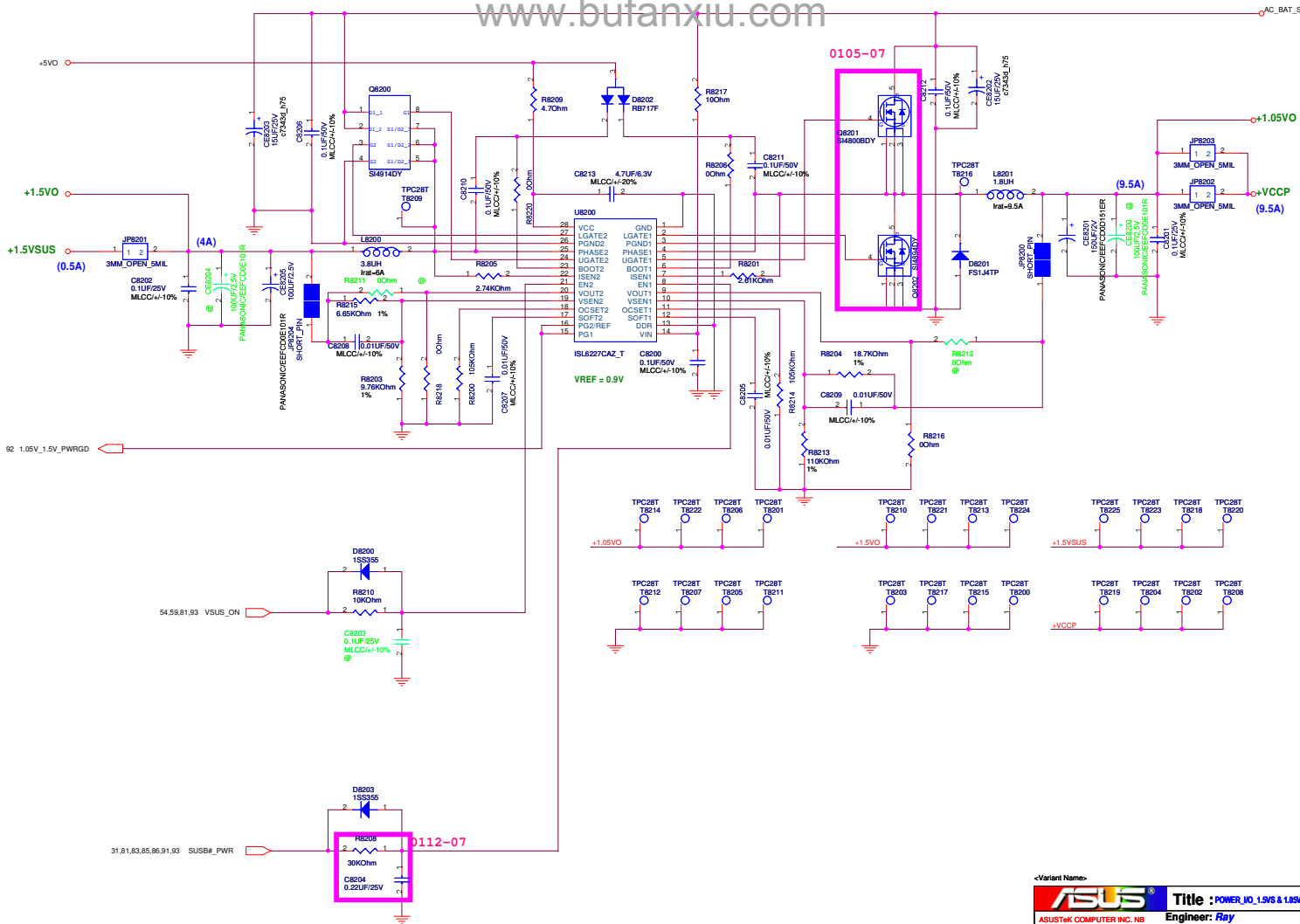
« Kennedy_Zhang »



<Variant Name>

ASUS		Title : POWER_SYSTEM	
ASUSTEK COMPUTER INC. NB	Engineer: Ray		
Size	Project Name	A7S	Rev
Custom			2.0
Date: 8/8/2007	8/21/2007	Sheet	81 of 88

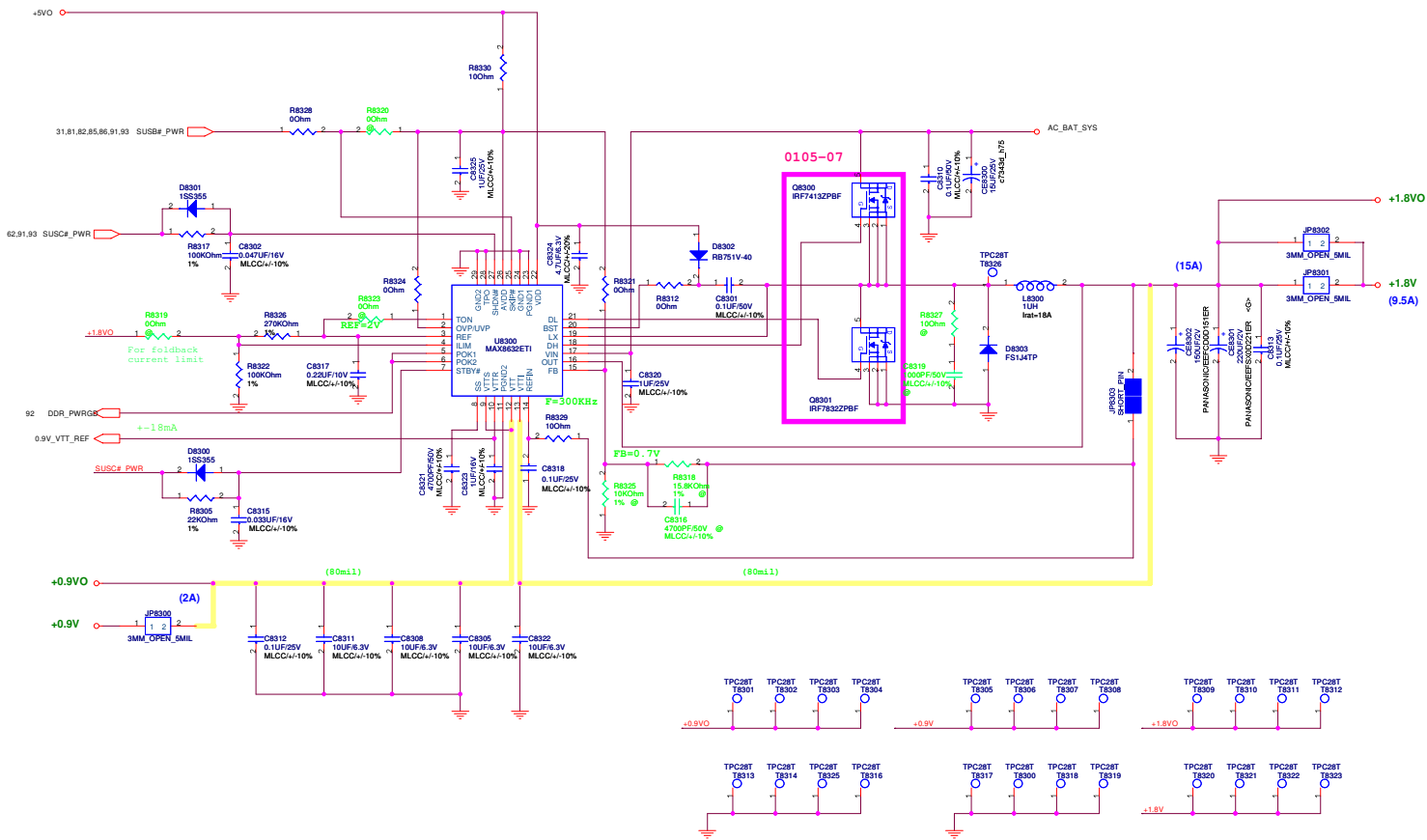
« Kennedy_Zhang »



<Variant Name>

	Title : POWER_IO_1.5V5 & 1.5V5
ASUSTEK COMPUTER INC. NB	Engineer: Ray
Size	Project Name
Custom	A7S
Date: 2007-11-21	Sheet: 82 of 85
Rev	2.0

<< Kennedy_Zhang >>



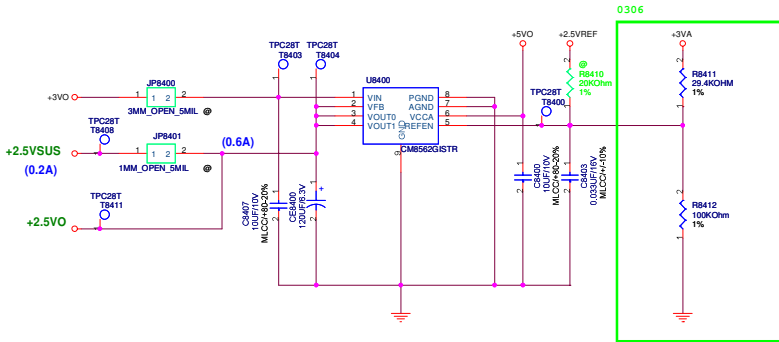
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ASUS		Title : POWER_IO_DDR & VTT	
ASUSTeK COMPUTER INC. NS		Engineer: Ray	
Size	Project Name	Rev	2.0
Custom	A7S	Date: 11/11/2007	Sheet 83 of 88

<< Kennedy_Zhang >>

+2.5VS & +2.5VSUS

0704

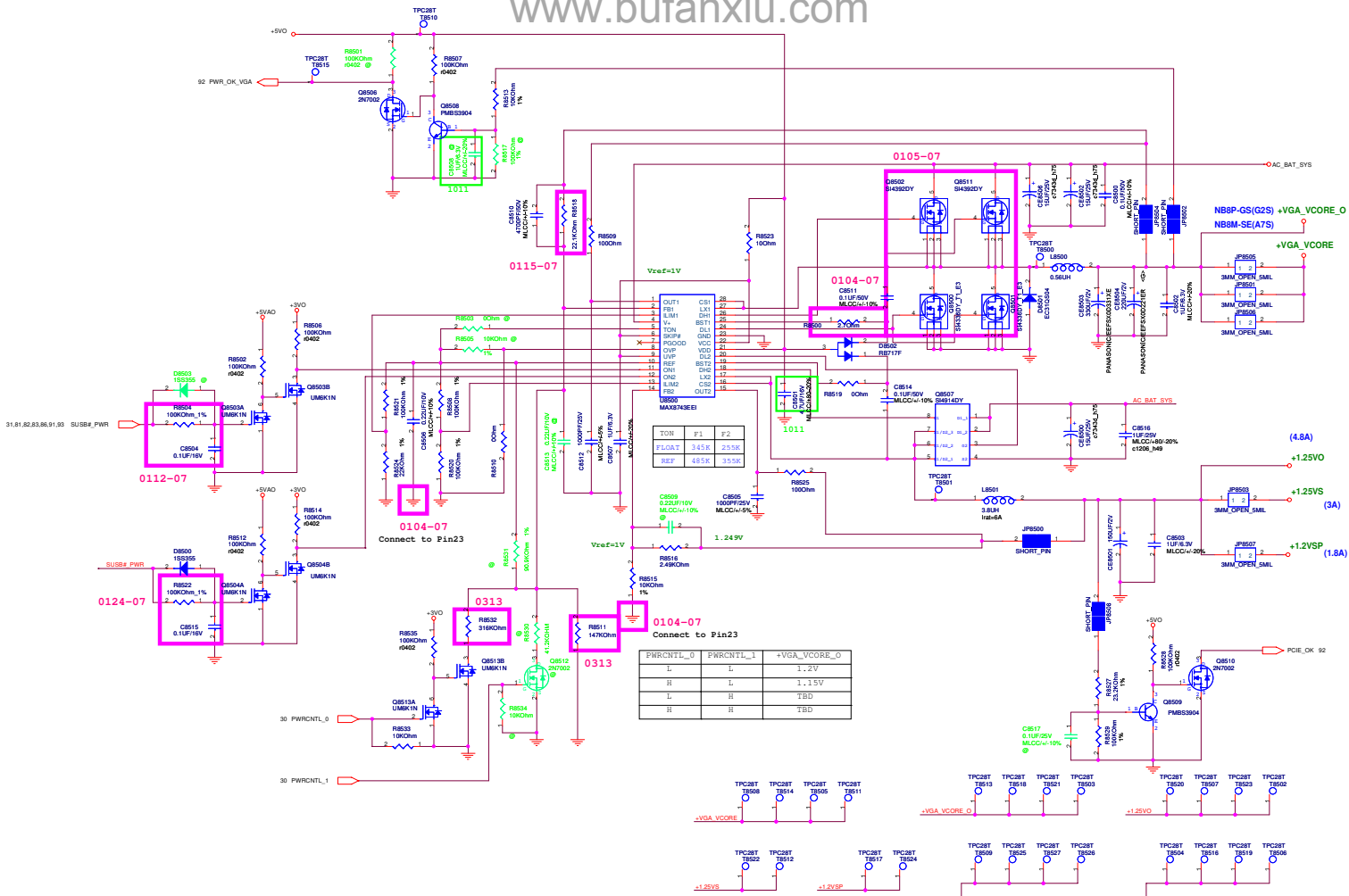


0306

<Variant Name>

ASUS		Title : POWER_IO_+2.5VS	
ASUSTEK COMPUTER INC. NB		Engineer: Ray	
Size	Project Name	Rev	
Custom	A7S	2.0	
Date: 2007-08-21		Sheet: 84	of 85

« Kennedy_Zhang »

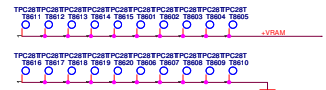
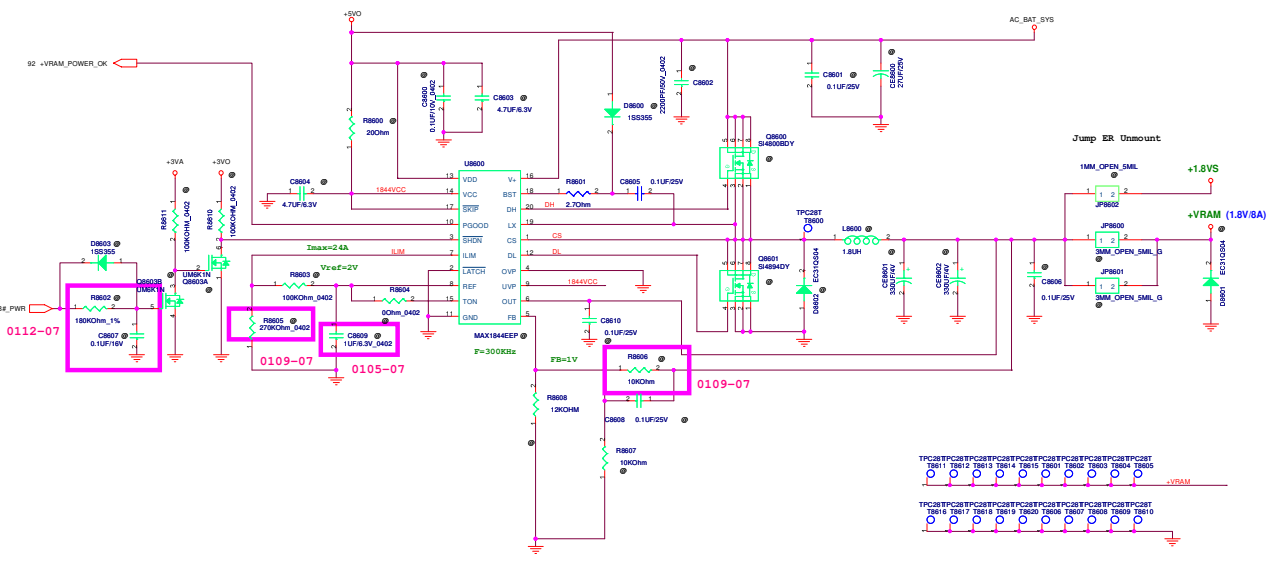


TON	F1	F2
FL0AT	345K	255K
REF	495K	335K

PWRCNTL_0	PWRCNTL_1	+VGA_VCORE_O
L	L	1.2V
L	L	1.15V
L	L	TBD
H	H	TBD

ASUS Title :POWER_VGA_CORE & RAM
 Engineer: Ray
 Rev 2.0
 Date: 11-21-2007

« Kennedy_Zhang »

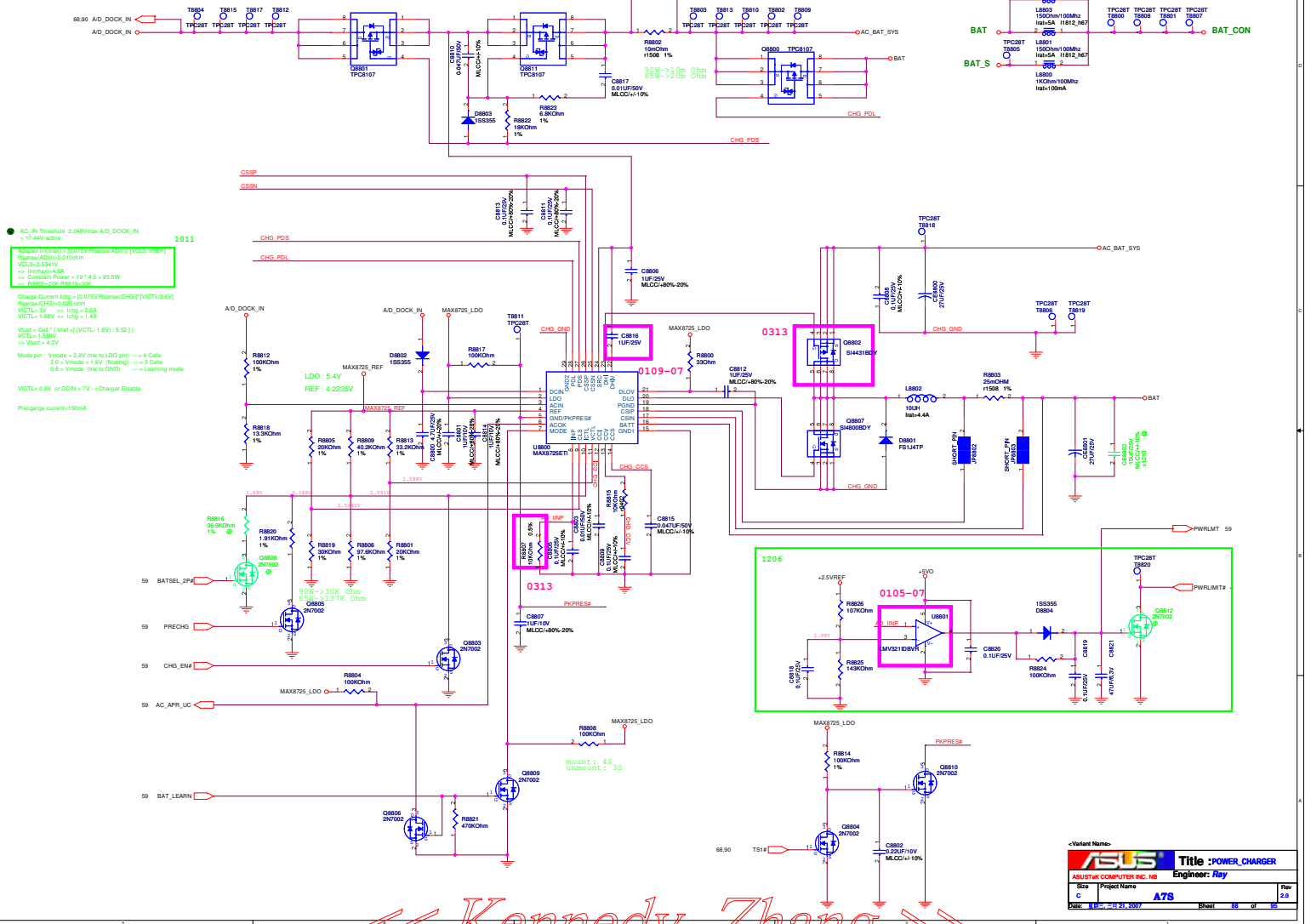


-Variant Name-		Title :-VDRAM	
ASUS		Engineer: Ray	
Sr#	Project Name	Rev	Rev
C	A7S	2.0	
Date: 11-21-2007	Sheet	85	of 95


<< Kennedy_Zhang >>

<Variant Name>				Title : POWER_SHUTDOWN#	
ASUSTeK COMPUTER INC. NB		Engineer: Ray			
Site	Project Name			Rev	
Custom	A7S			2.0	
Date: 8/21/2007				Sheet 87	of 95

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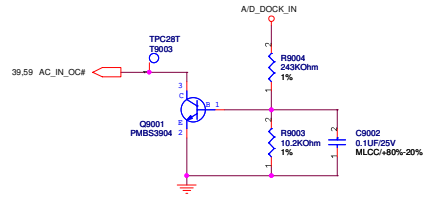
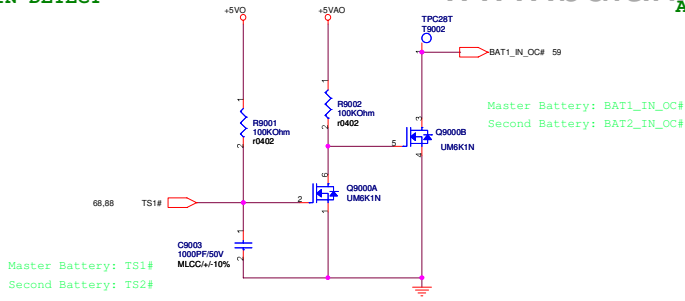
« Kennedy_Zhang »

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ASUSTeK COMPUTER INC. NB		Engineer: Ray
Site	Project Name	Rev
Custom	A7S	2.0
Date: 8/21/2007		Sheet 89 of 89

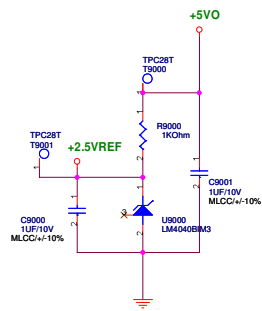
« Kennedy_Zhang »

BATTERY IN DETECT

ADAPTER IN DETECT



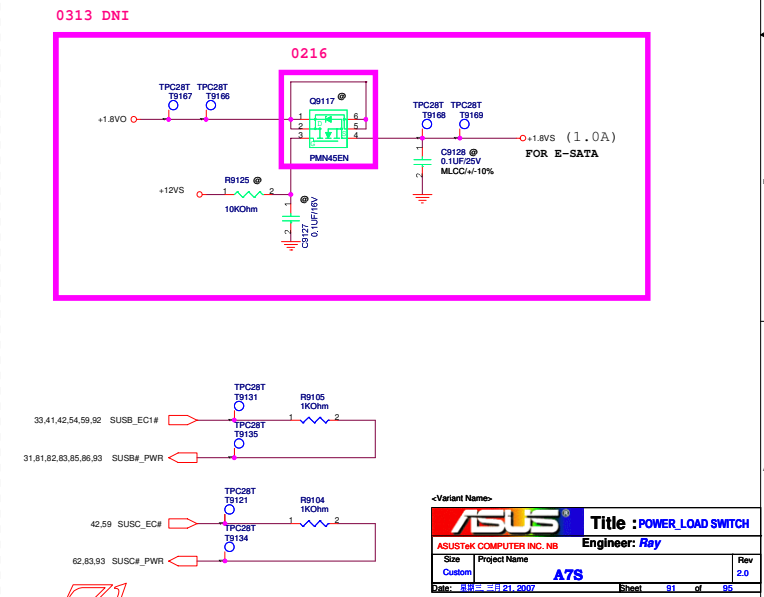
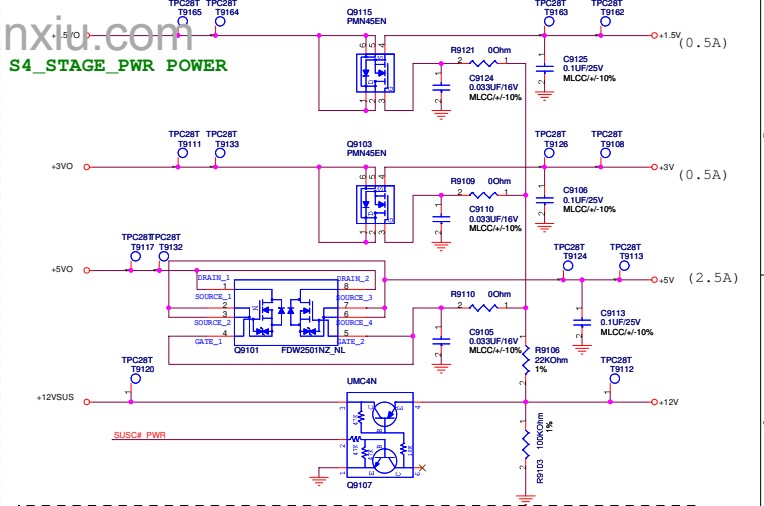
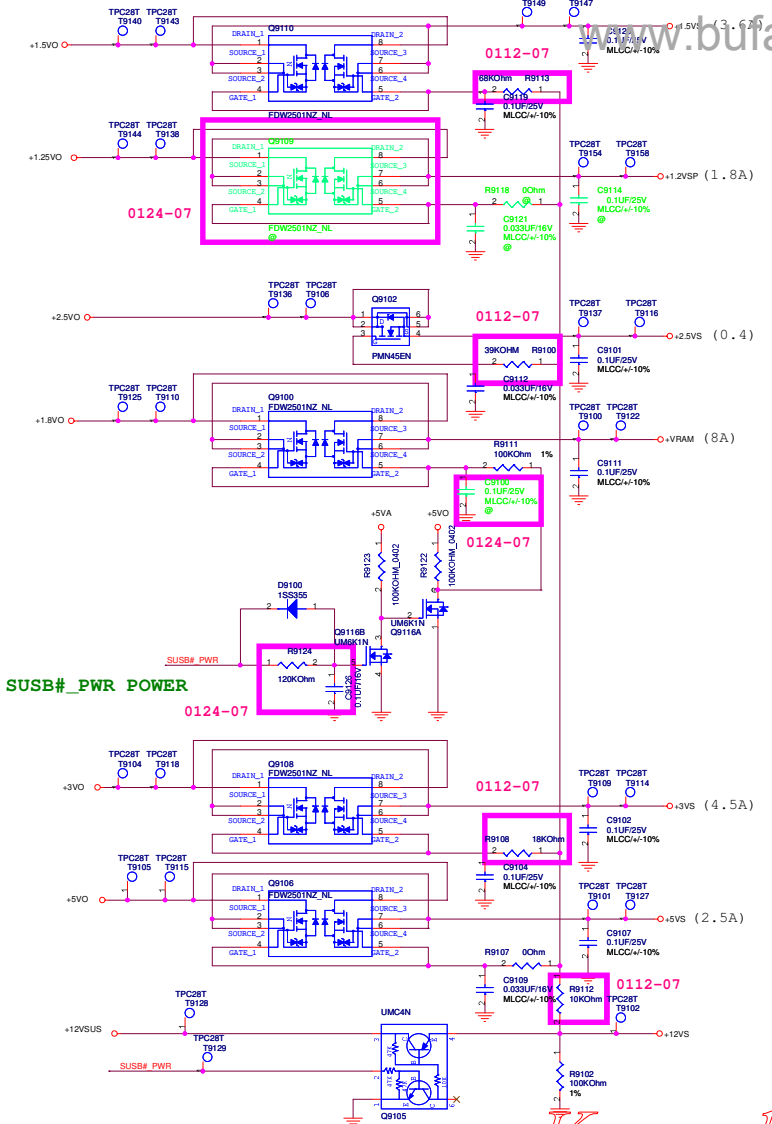
+2.5VREF



<Variant Name>

ASUS		Title : POWER_DETECT	
ASUSTeK COMPUTER INC. NB		Engineer: Ray	
Size	Project Name		Rev
Custom	A7S		1.1
Date: 2007-08-21		Sheet	90 of 95

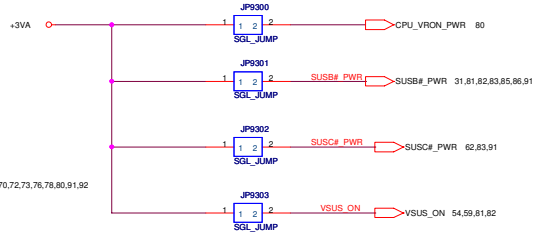
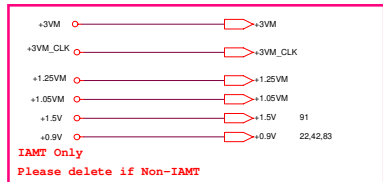
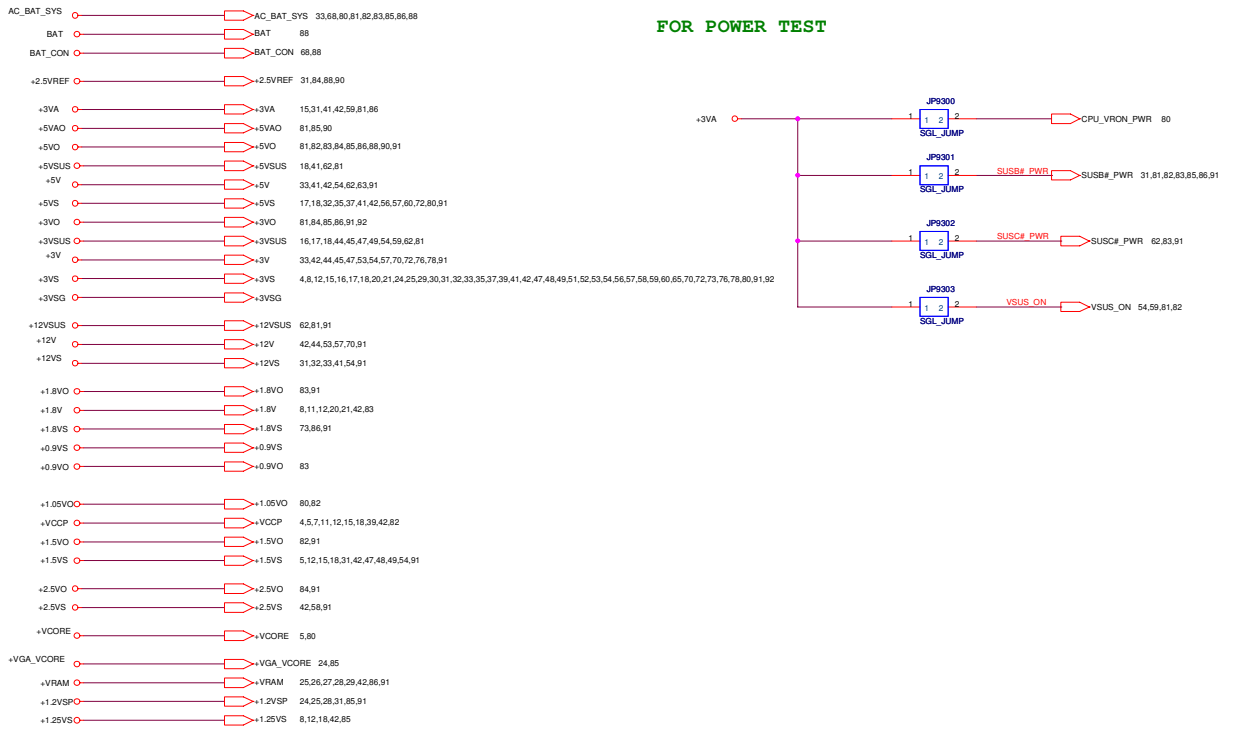
« Kennedy_Zhang »



« Kennedy_Zhang »

ASUS		Title : POWER_LOAD_SWITCH	
ASUSTek COMPUTER INC. NB		Engineer: Ray	
Size	Project Name	Custom	Rev
Date: 8/21/2007	A7S		2.0
Date: 8/21/2007		Sheet 91 of 95	

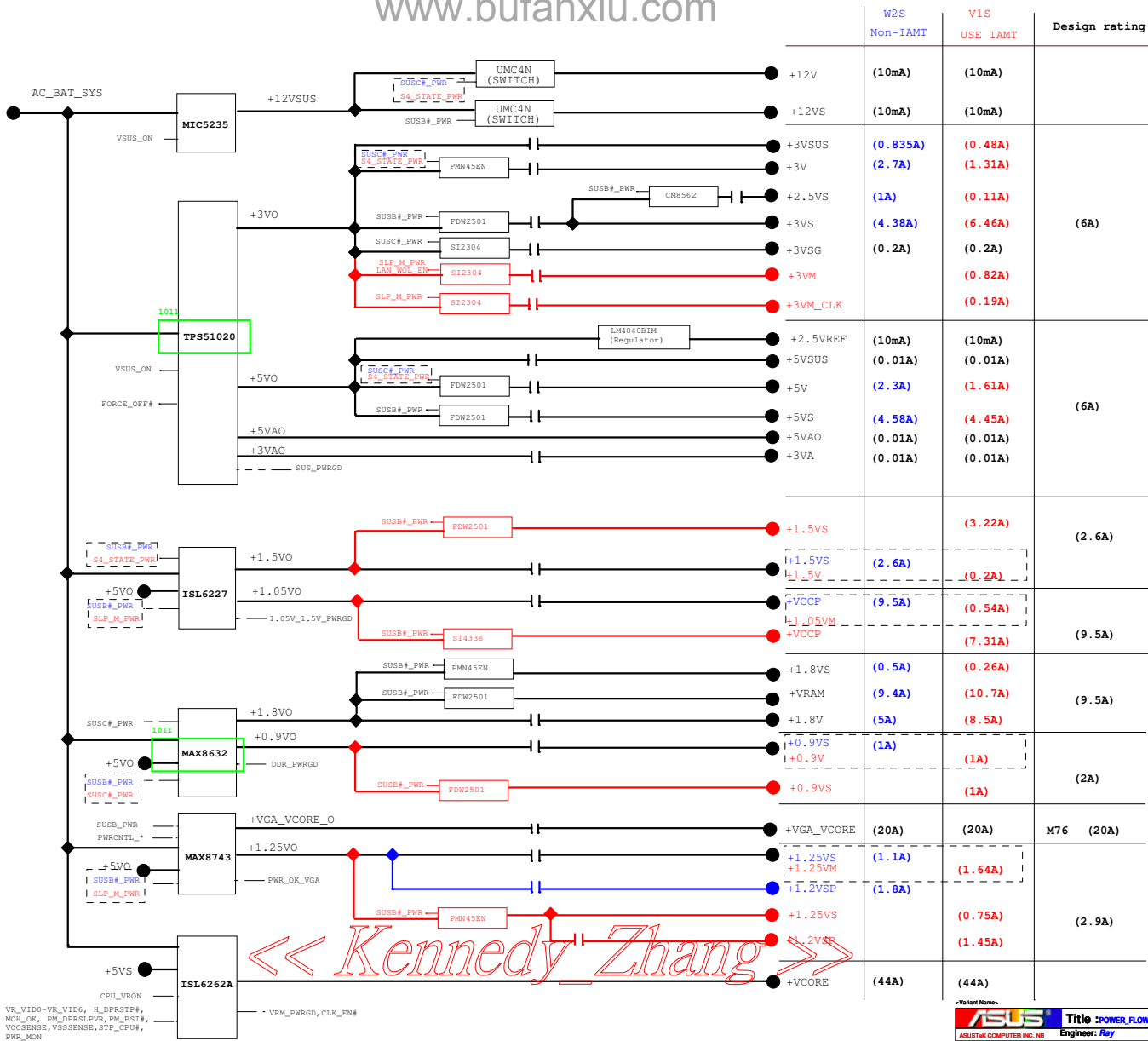
FOR POWER TEST



<Variant Name>

ASUS		Title : POWER_SIGNAL	
ASUSTek COMPUTER INC. NB		Engineer: Ray	
Site	Project Name	Rev	
Custom	A7S	2.0	
Date: 2007-11-21		Sheet	93 of 95

« Kennedy_Zhang »



« Kennedy Zhang »

VR_VID0-VR_VID6, H_DPRSTP#, MCH_OK, PM_DPRSIPVR, PM_PSI#, VCCSENSE, VSSSENSE, STP_CPU#, PWR_MON

ASUS Title: POWER_FLOWCHART
 ADUSTAK COMPUTER INC. NB Engineer: Ray
 Size Project Name
 Control AYS Rev
 Date: 11-15-2007 Sheet 01 of 02

Rev	Date	Description
1.0	10/04/05	1. Initial release.
1.1	12/26/05	1. Remove CardReader PME schemaic for R5C832 CardReader.[16,49] 2. Change X6 32.768MHz source.[59] 3. Remove FAN control schematic of the 3PIN.[37] 4. Remove EC control pin for Fan control schmatic.[59] 5. Delete VGA Thermal OTEMP# 0 ohm resistor .[37] 6. Unmount USB 47uF for dual USB connector .[62] 7. Delete REAR_R & REAR_L 1uF and 220 ohm.[56] 8. Add 4 pcs 10uF (0805) for +VRAM.[28] 9. Change TPLED_ON GPIO pin from GPIO19 to GPIO14 and 10K ohm pull low.[17,66] 10. Change LAN, CRT, DVI, USB, SVIDEO, SATA, PATA, DC, BAT, NewCard, MIC connector.[45,32,35,62,35,72,72,68,68,52,57] 11. USB Schematic modify.[62] 12. Delete U38 BIOS Socket.[64] 13. PCB_ID from 000 change to 001.[17] 14. Reverse RN28 pin definition.[70] 15. CLK_TPMPPI termination resistor from 33 Ohm to 22 Ohm.[39] 16. Add Pre-Amplifier MIC circuit.[56,57] 17. Add H3,H4 NUT for FAN and modify screw hole H17, H20.[37,74] 18. Modify ACZ_BCLK_CODEC R407 & ACZ_BCLK_MOD R351 termination resistor.change to 22 Ohm and mount 22pF capacitor C482 & C575[15,45,56] 19. Modify SDWP net name.[50,51] 20. Add SD/MSCLK C641 10pF/50V.[51] 21. Ear output from FRONT R/L OUT change to SURR_OUT_R/L.[56,57] 22. Unmount C608 U32 Amplifier PC-BEEP capacitor.[57] 23. Add C885 LAN_GND capacitor for EMI request.[45] 24. Modify C676 & C677 1000pF/3KV capacitor for EMI request.[45] 25. Modify L121-L124 220 Ohm bead for EMI request.[57] 26. Modify RN55 0 Ohm for inverter signal.[33] 27. Modify CPU & VGA screw hole H39,H40,H43,H44,H47,H48,H51.[74]

Rev	Date	Description

		Title : History	
ASUSTek COMPUTER INC		Engineer: Ray	
Site	Project Name	Rev	
Custom	ATS	3.1	
Date: 08-21-2007	Sheet 05 of 05		

« Kennedy_Zhang »